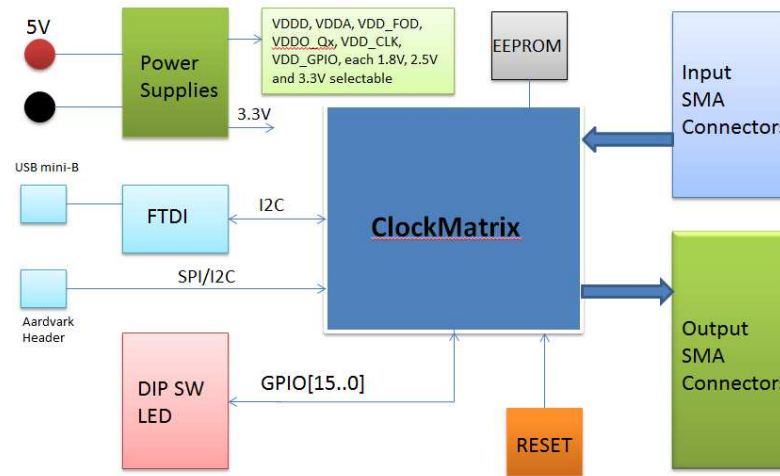
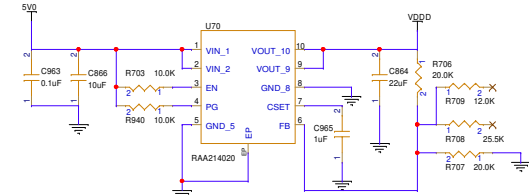
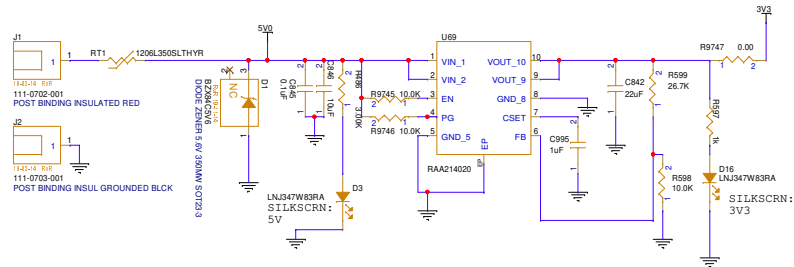


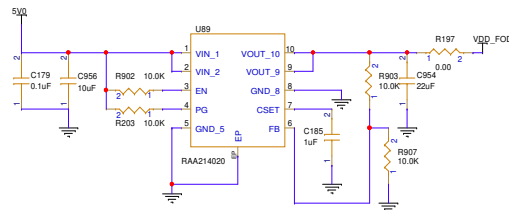
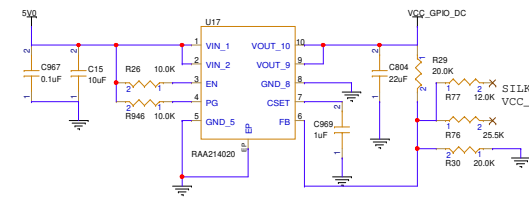
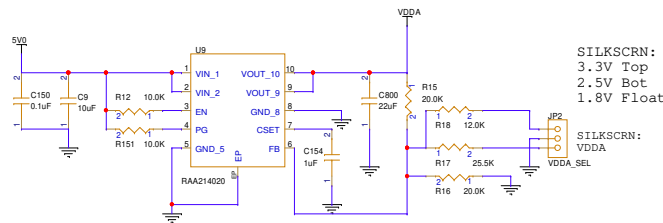


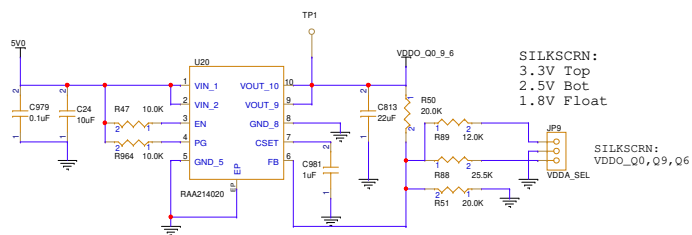
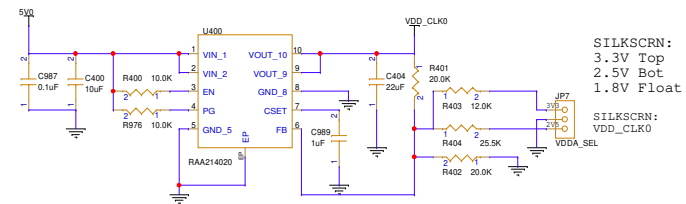
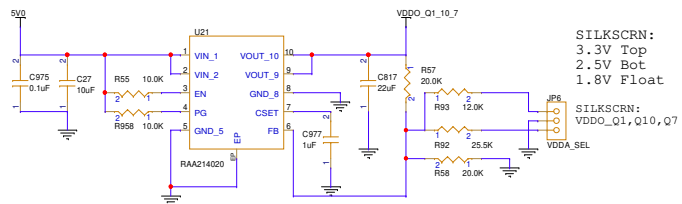
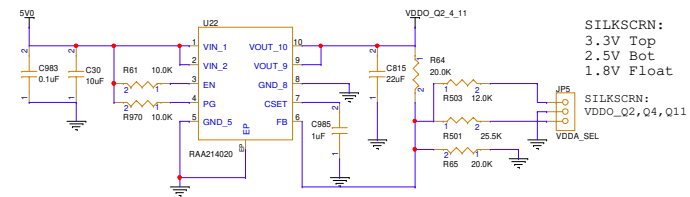
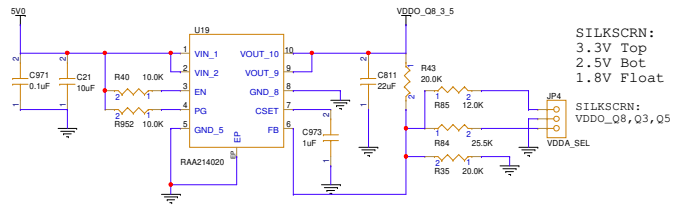
Board 3V3



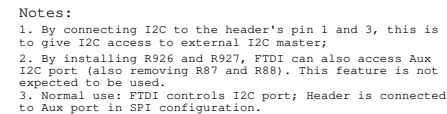
SILKSCRN:
3.3V Top
2.5V Bot
1.8V Float

SILKSCRN:
VDD0



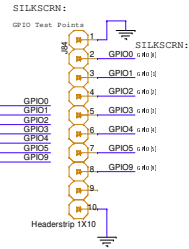
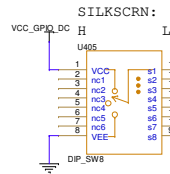
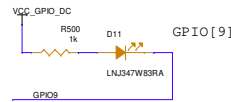
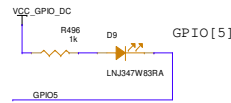
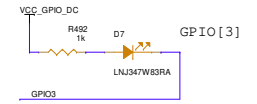
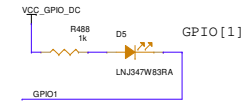
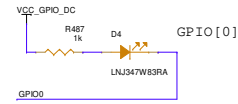


		Renesas 603 March RD Kanata, ON K2K 2M5	
Title: CM 8A34xxxx_72QFN			
Size	HW Number	Engineer:	Rev.
C	HW#	Kashan Ramachandran	B
Date: 11/28/2022		Sheet: 3 of 10	

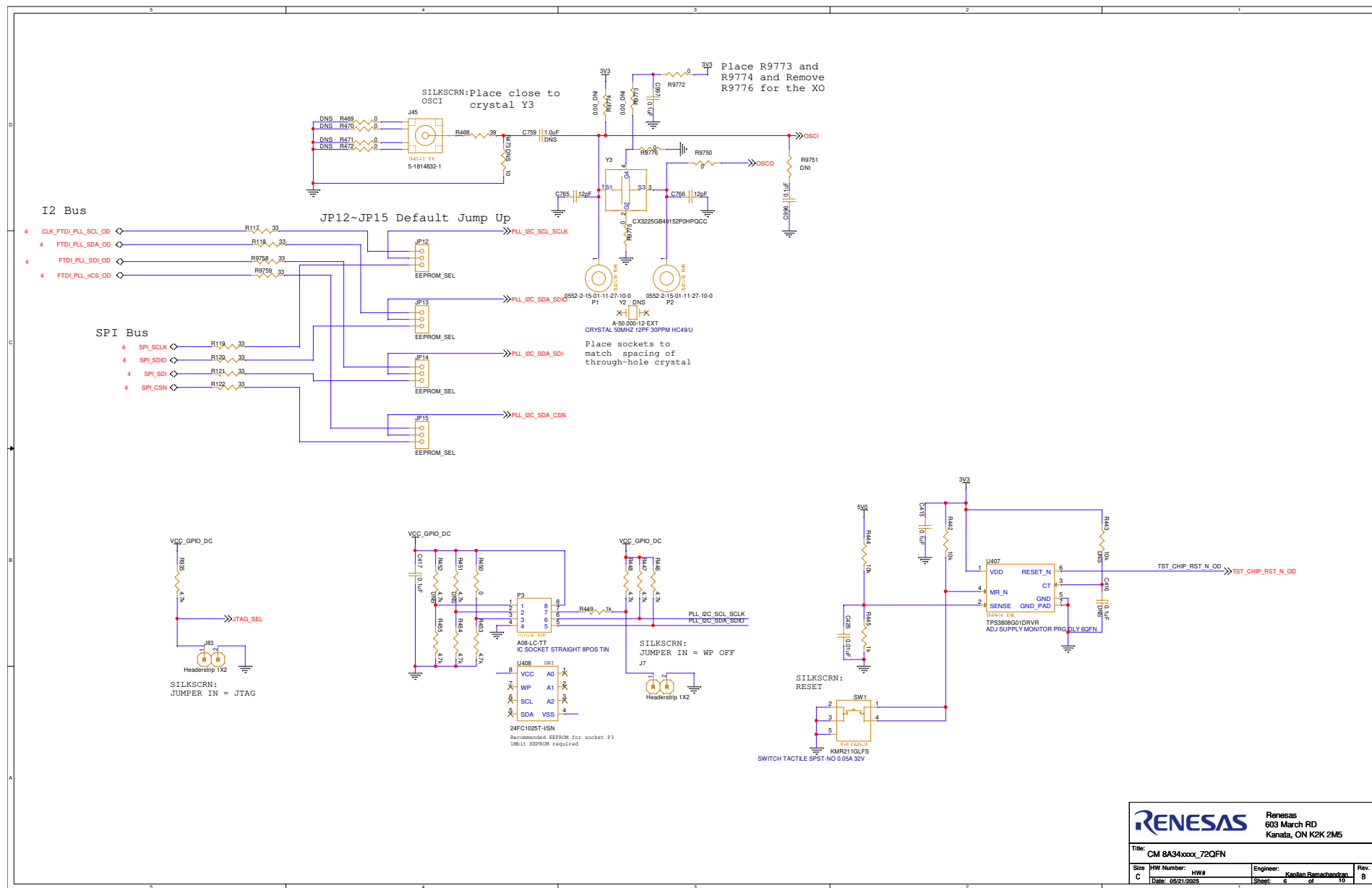


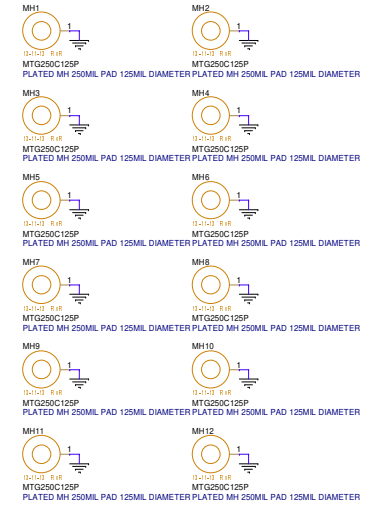
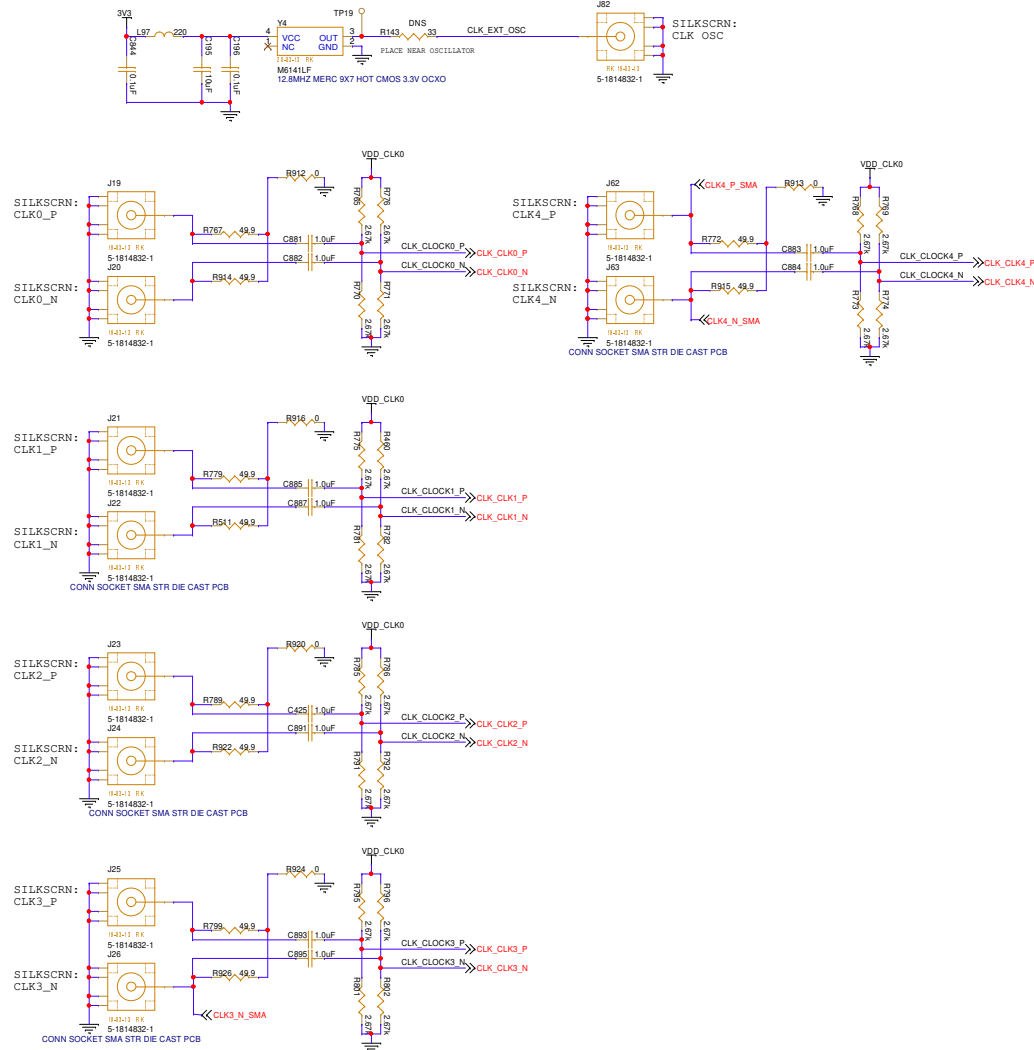
Note for Silkscreen:
Place GPIO[x] label close to each
corresponding LED and Test point.

- 6 GPIO0
- 6 GPIO1
- 6 GPIO2
- 6 GPIO3
- 6 GPIO4
- 6 GPIO5
- 6 GPIO9



		Renesas 603 March RD Kanata, ON K2K 2M5	
Title: CM 8A34xxxx_72QFN			
Size	HW Number: HW#	Engineer: Kapilan Ramachandran	Rev. B
C	Date: 01/30/2023	Sheet: 5 of 10	





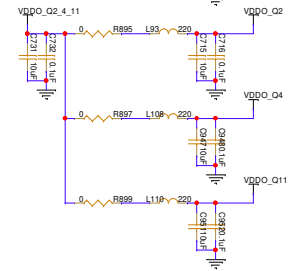
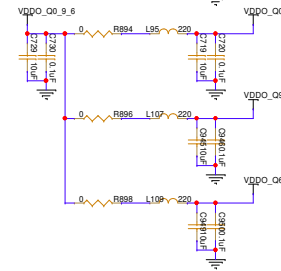
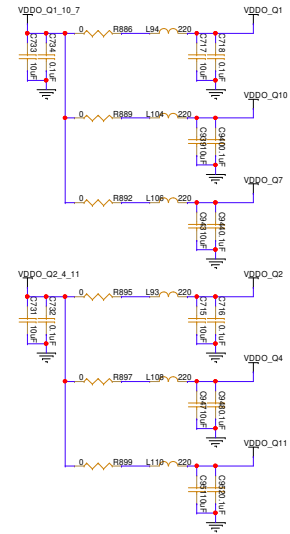
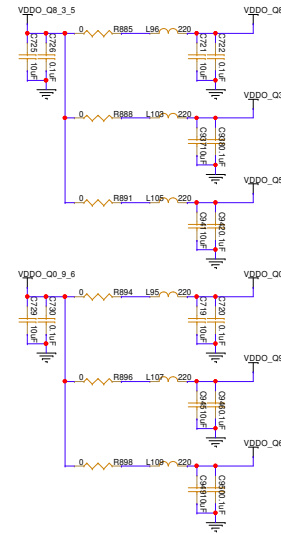
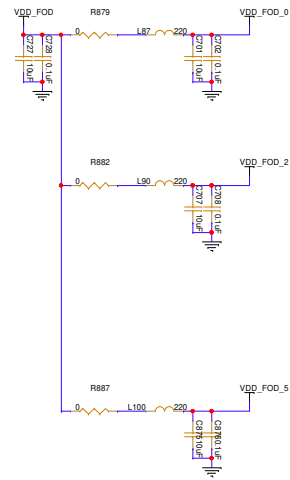
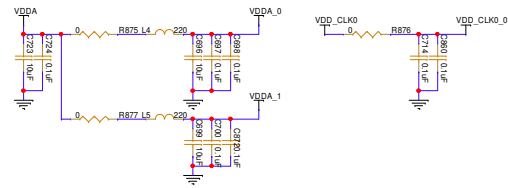
		Renesas 603 March RD Kanata, ON K2K 2M5	
Title: CM 8A34xxxx_72QFN			
Size	HW Number:	Engineer:	Rev.
C	HW#	Kashan Ramachandran	B
Date: 11/28/2022		Sheet: 7 of 10	

PLACE PARALLEL TERMINATIONS CLOSE TO U58

Diagram showing 12 clock signal pairs (CLK_Q0_P/N to CLK_Q11_P/N) connected to SMA connectors (J29 to J74) with parallel terminations (R741 to R784) and silk labels (SILKSCRN: Q0_P, Q0_N, etc.). The diagram includes a note: "PLACE PARALLEL TERMINATIONS CLOSE TO U58".

Bottom right corner contains the Renesas logo and a table with HW Number, Date, Engineer, and Sheet information.

RENESAS Renesas 603 March RD Kanata, ON K2K 2M5			
Title: CM 8A34xxx_72QFN			
Size	HW Number:	Engineer:	Rev.
C		Kaushal Ramchandran	B
Date:	11/28/2022	Sheet:	8 of 10



RENESAS			
Renesas 603 March RD Kanata, ON K2K 2M5			
Title: CM 8A34xxxx_72QFN			
Size	HW#	Engineer:	Rev.
C		Kashan Ramachandran	B
Date:	11/28/2022	Sheet:	9 of 10

