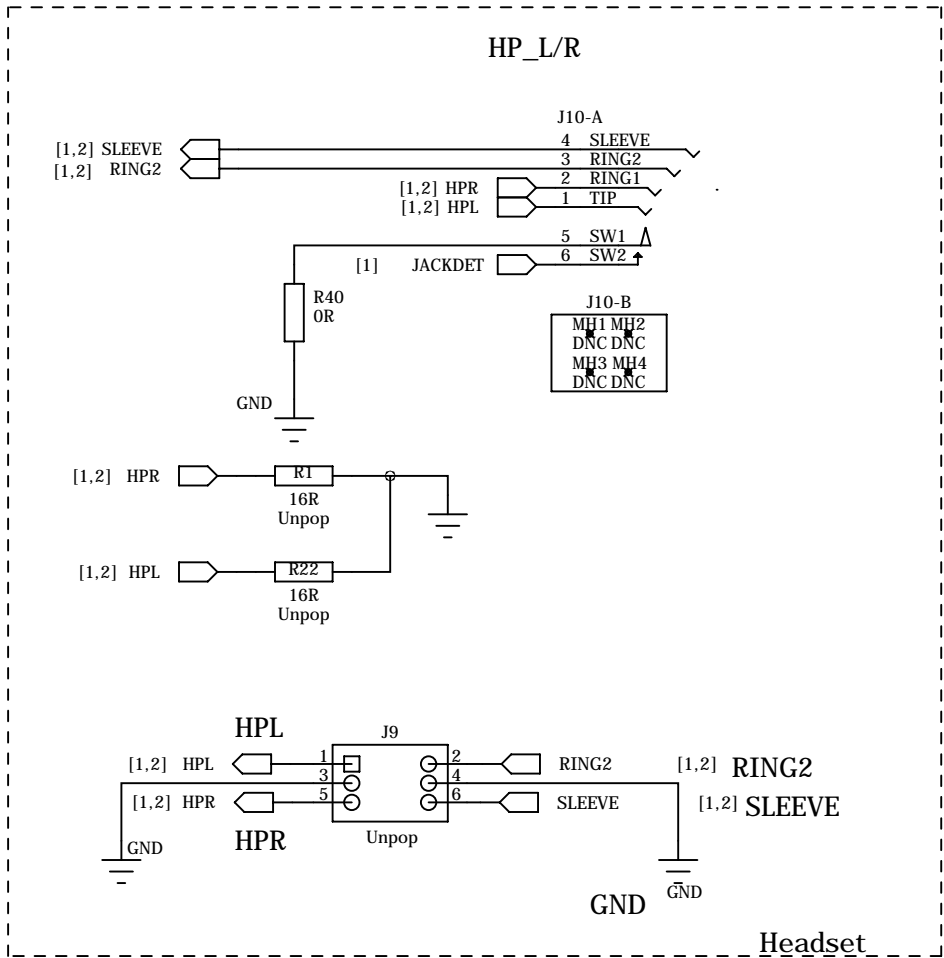
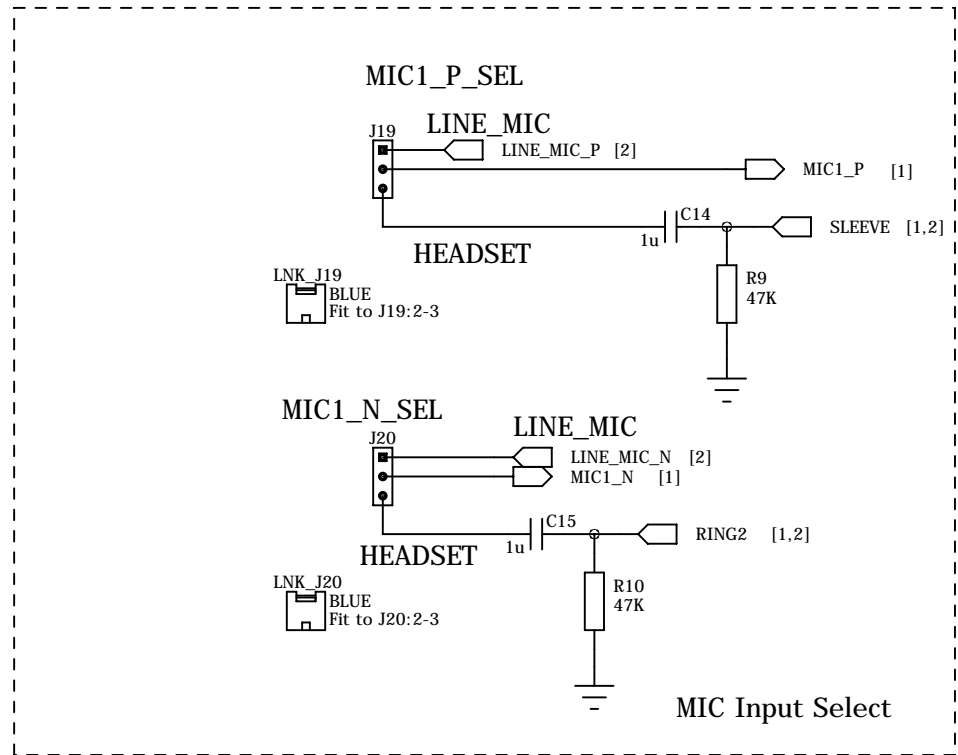
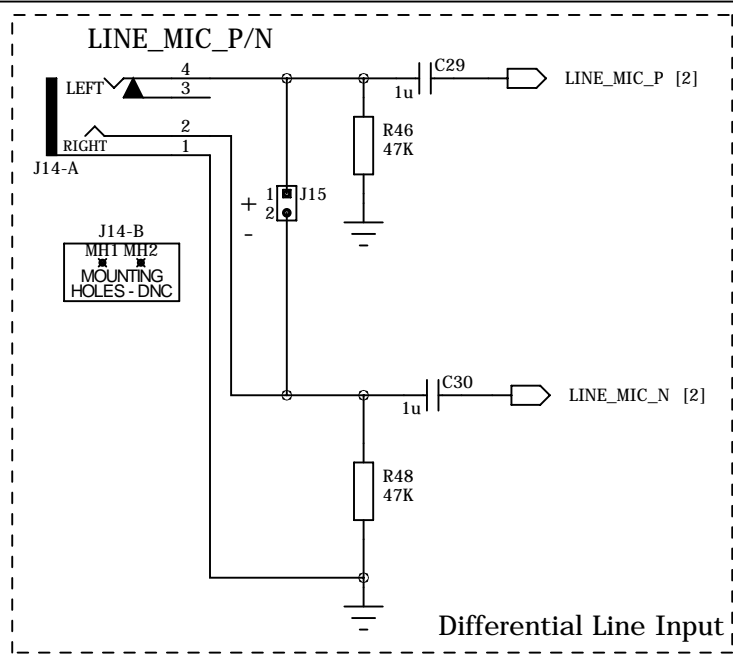


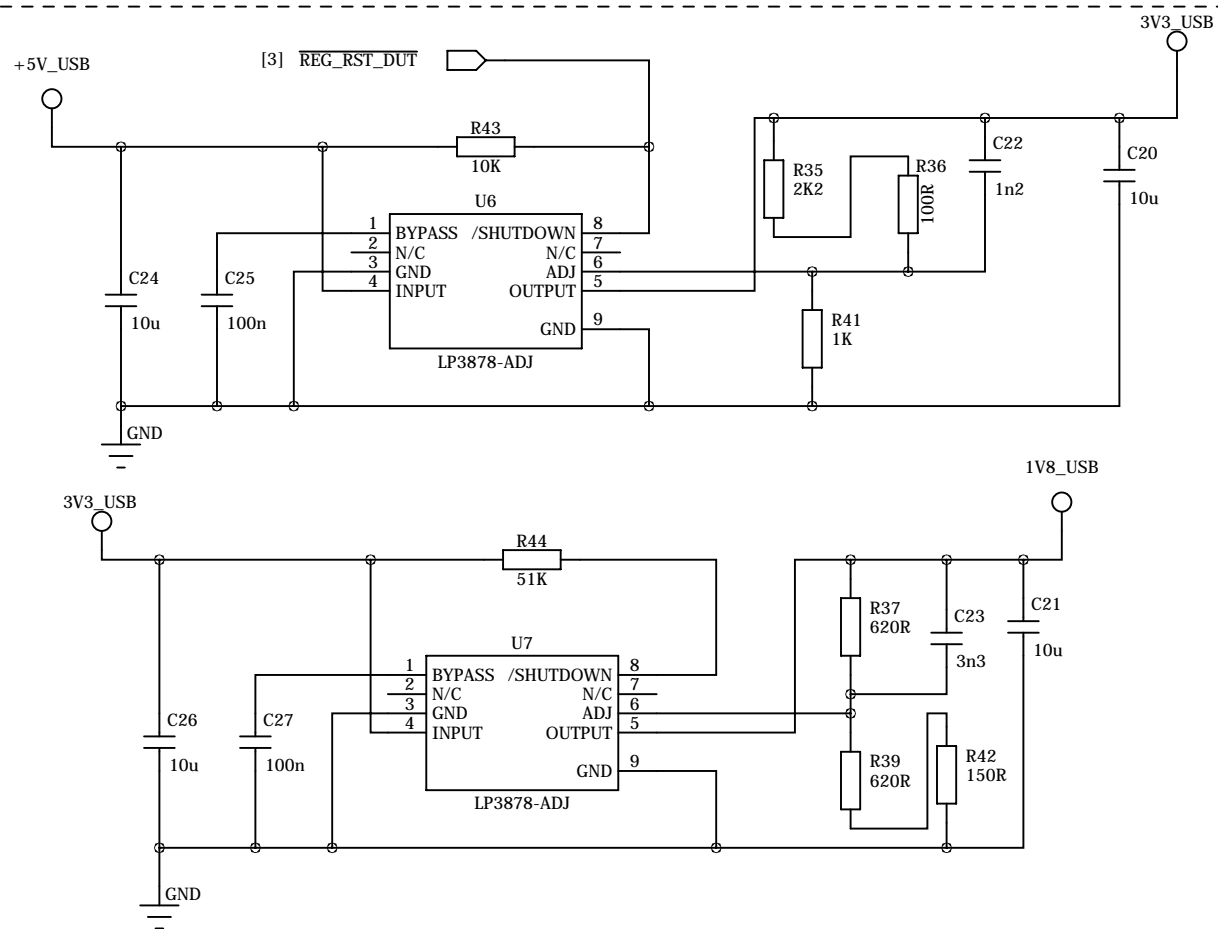
CONFIDENTIAL INFORMATION

PREPARED:	DATED:
CM	26/10/2015
RESPONSIBLE:	DATED:
CM	26/10/2015

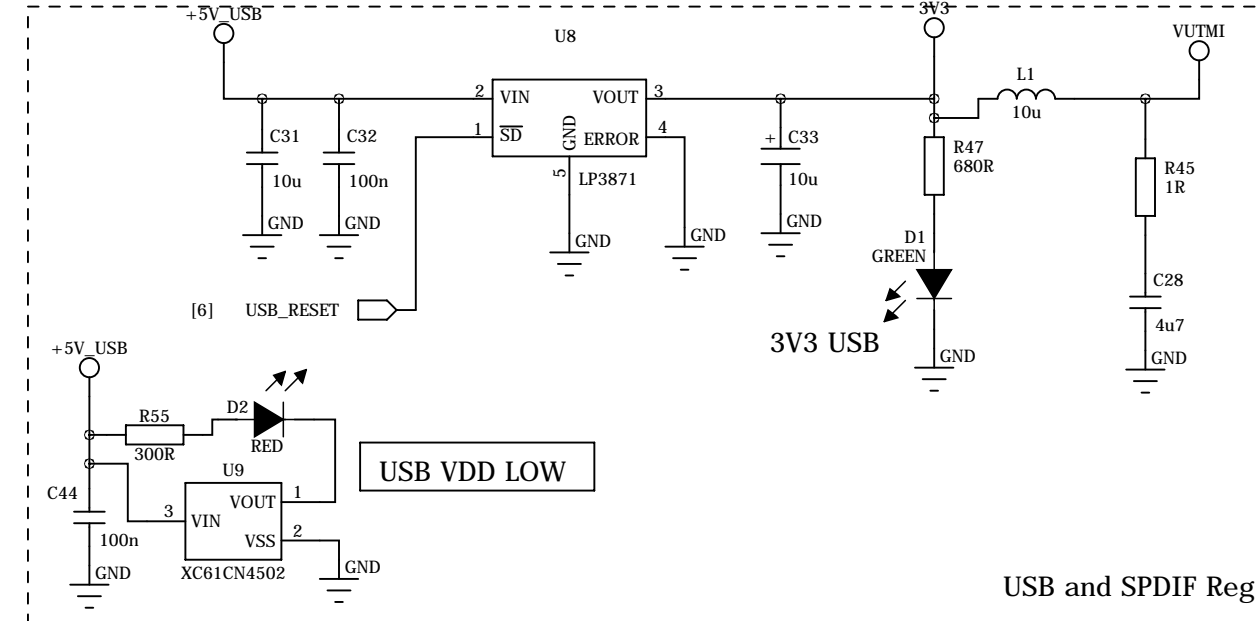
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DRAWING NO:	284-02-C	REV:	C
SHEET NAME:	DA7219		



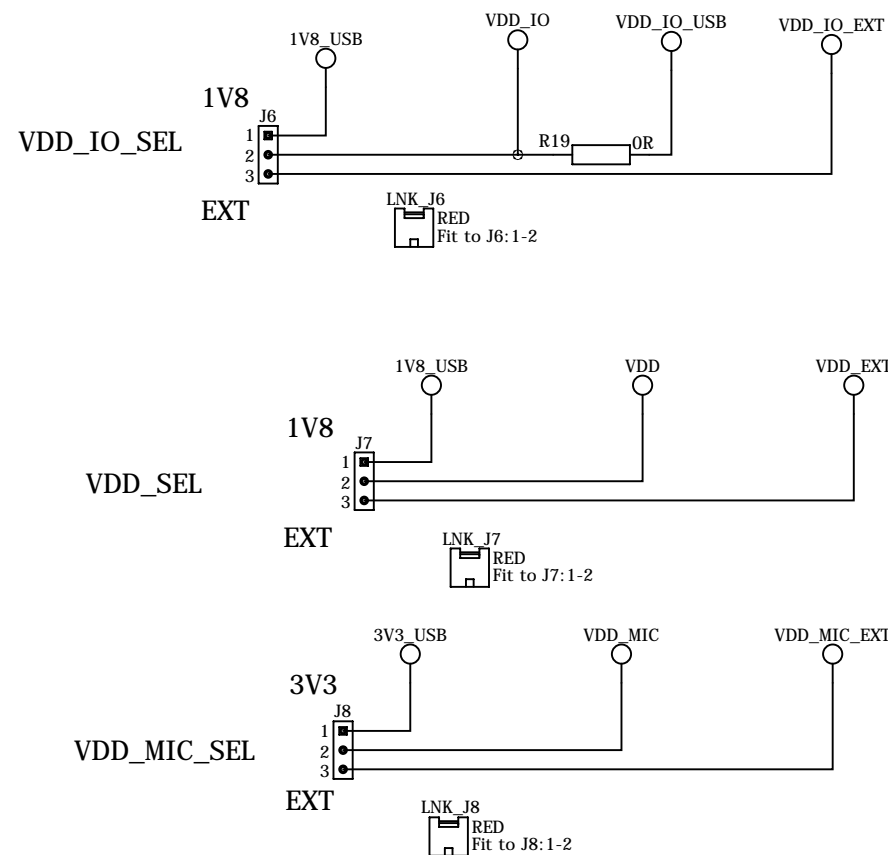
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PREPARED: CM	DATED: 26/10/2015	DRAWING NO: 284-02-C	REV: C	
RESPONSIBLE: CM	DATED: 26/10/2015	SHEET NAME: IO		PAGE: 2 OF 8



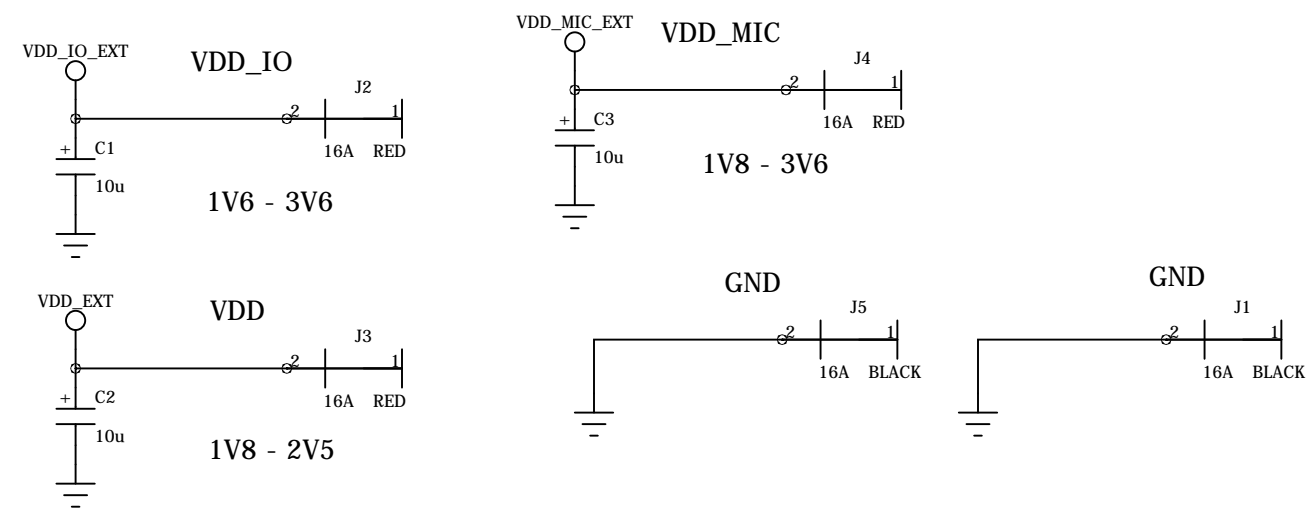
DUT/IOVDD VOLTAGE Regs



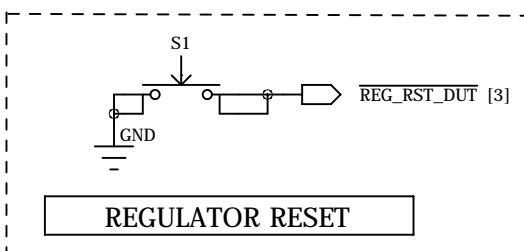
USB and SPDIF Reg



Power Select Headers



External Power Connectors



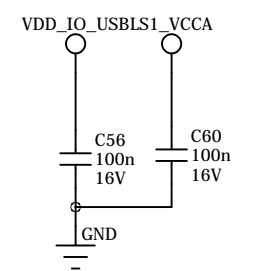
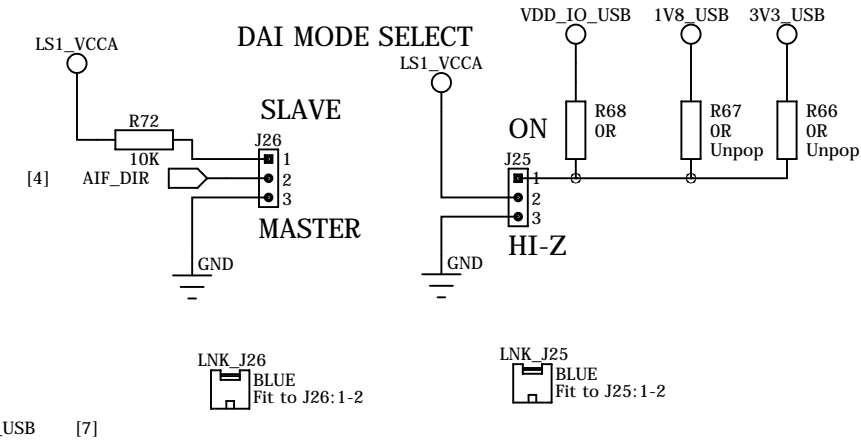
REGULATOR RESET

CONFIDENTIAL INFORMATION

PREPARED:	DATED:
CM	26/10/2015
RESPONSIBLE:	DATED:
CM	26/10/2015

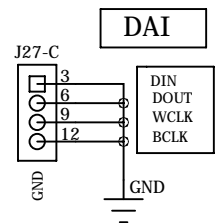
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DRAWING NO:	284-02-C	REV:	C
SHEET NAME:	POWER		PAGE:
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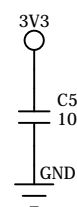
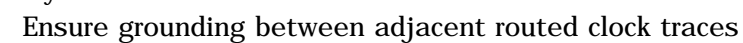


Ensure grounding between adjacent routed clock traces

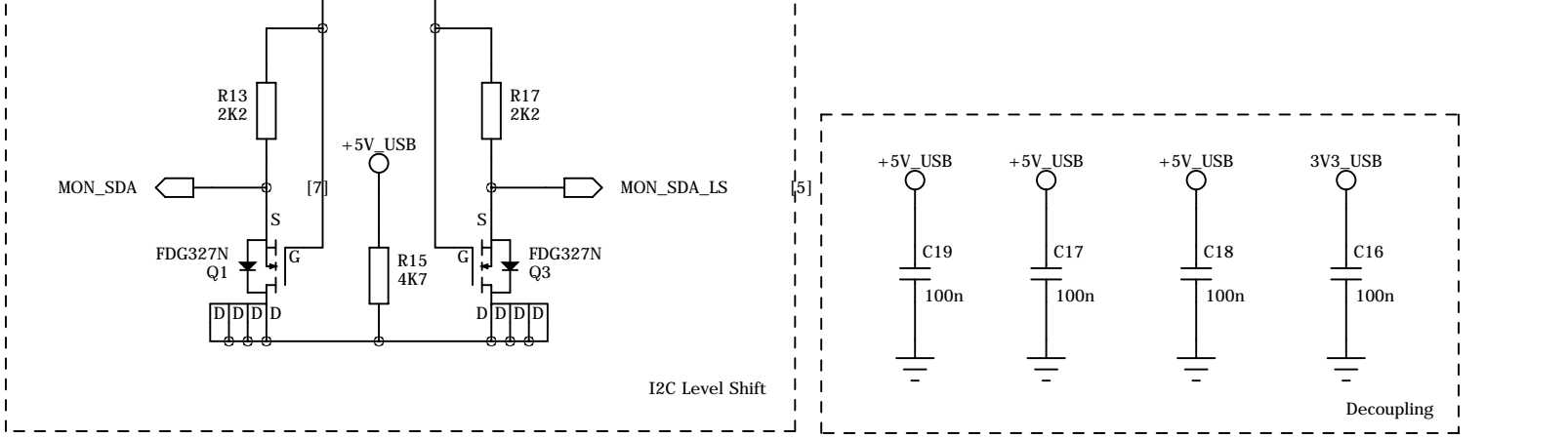
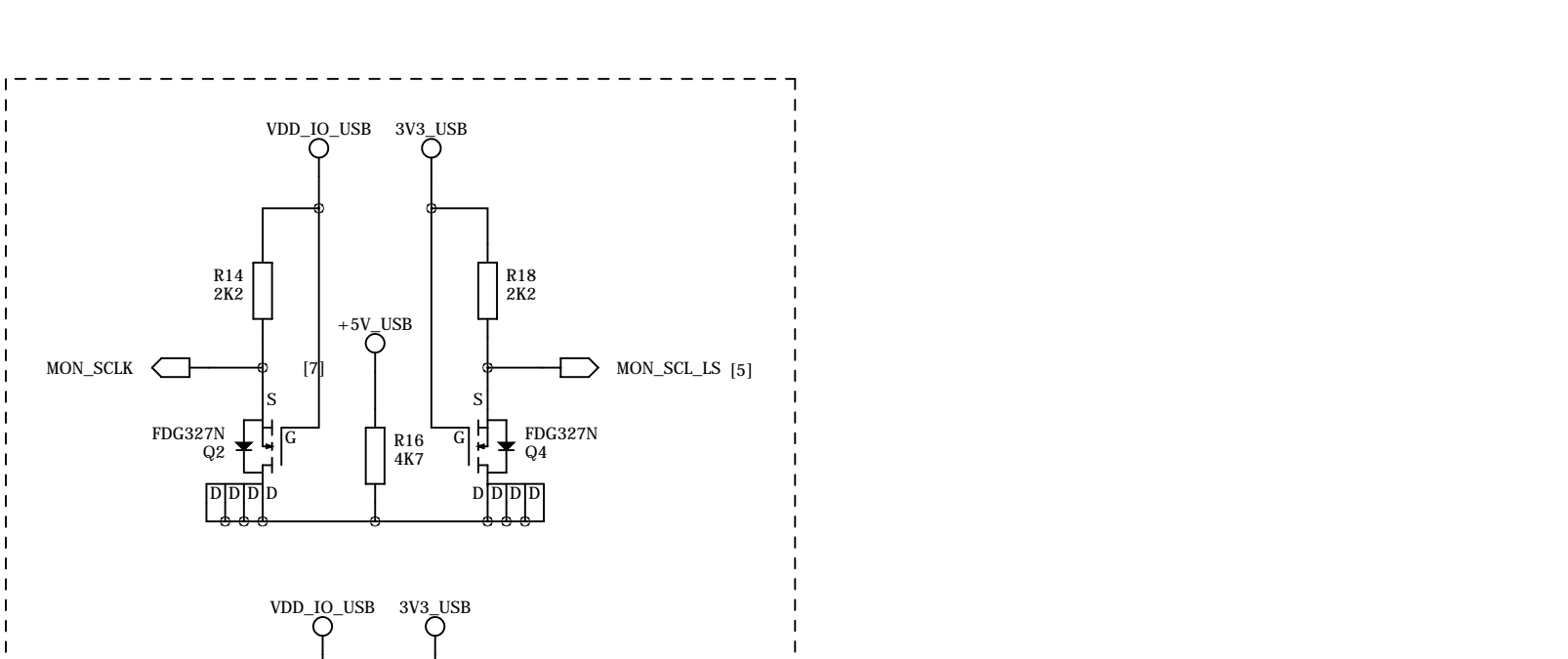
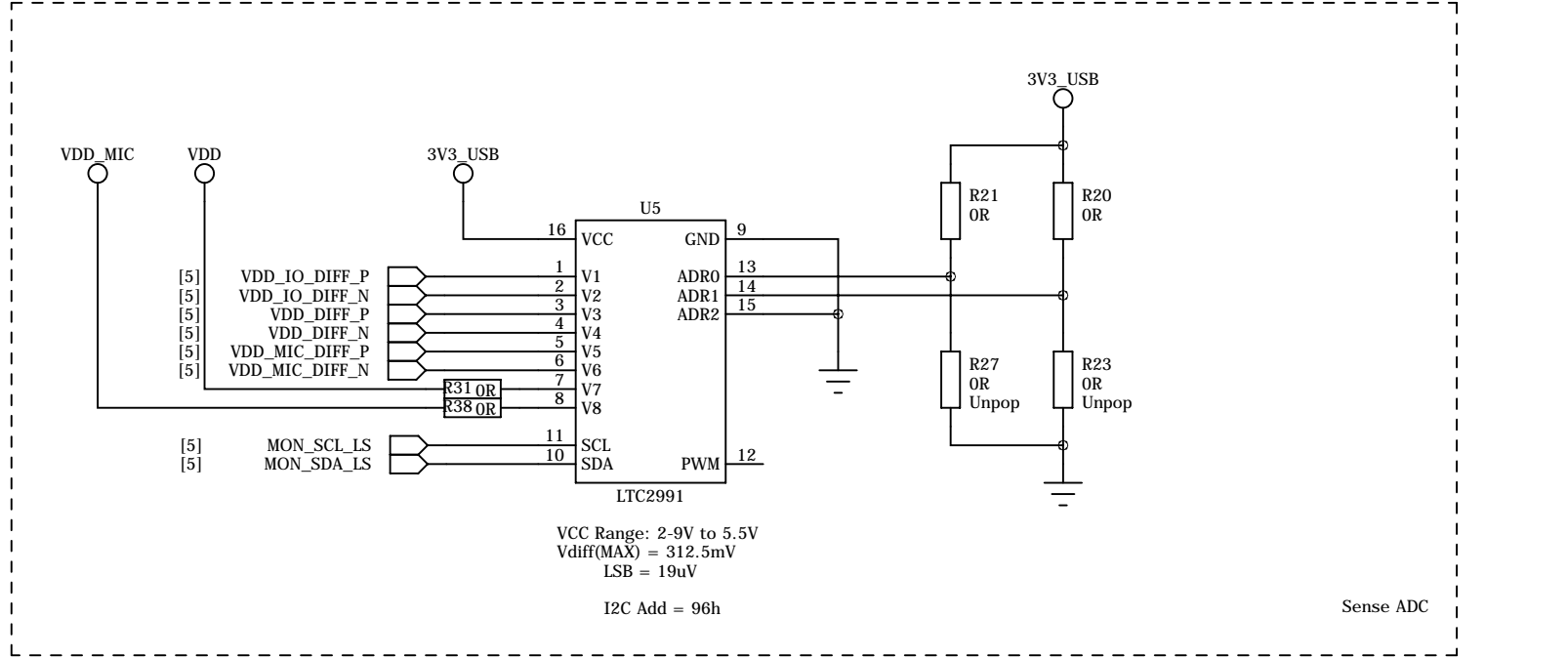
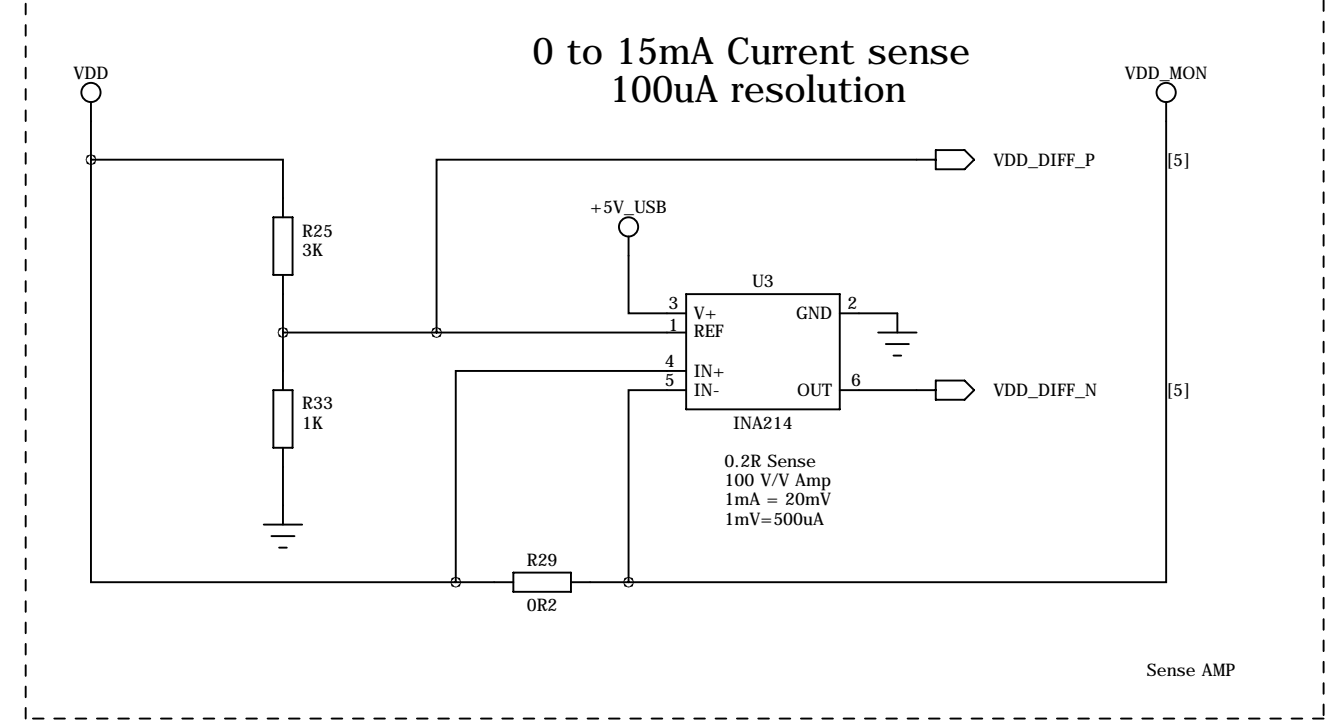
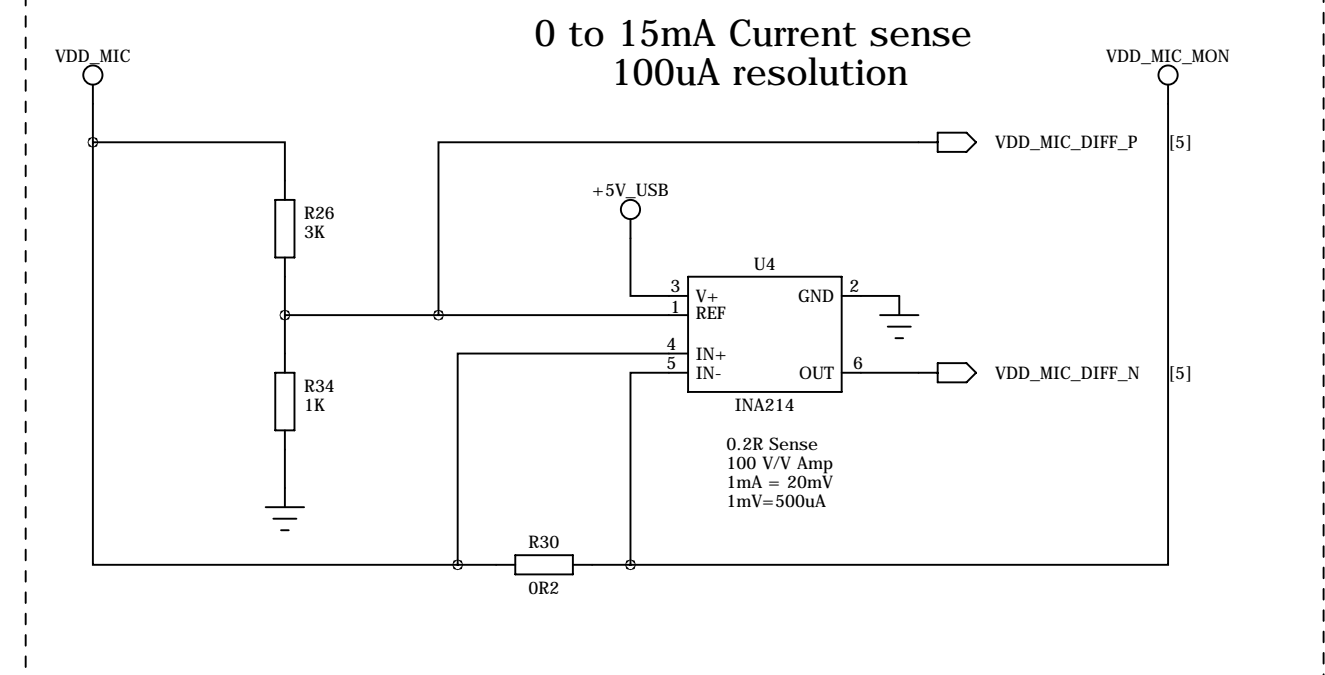
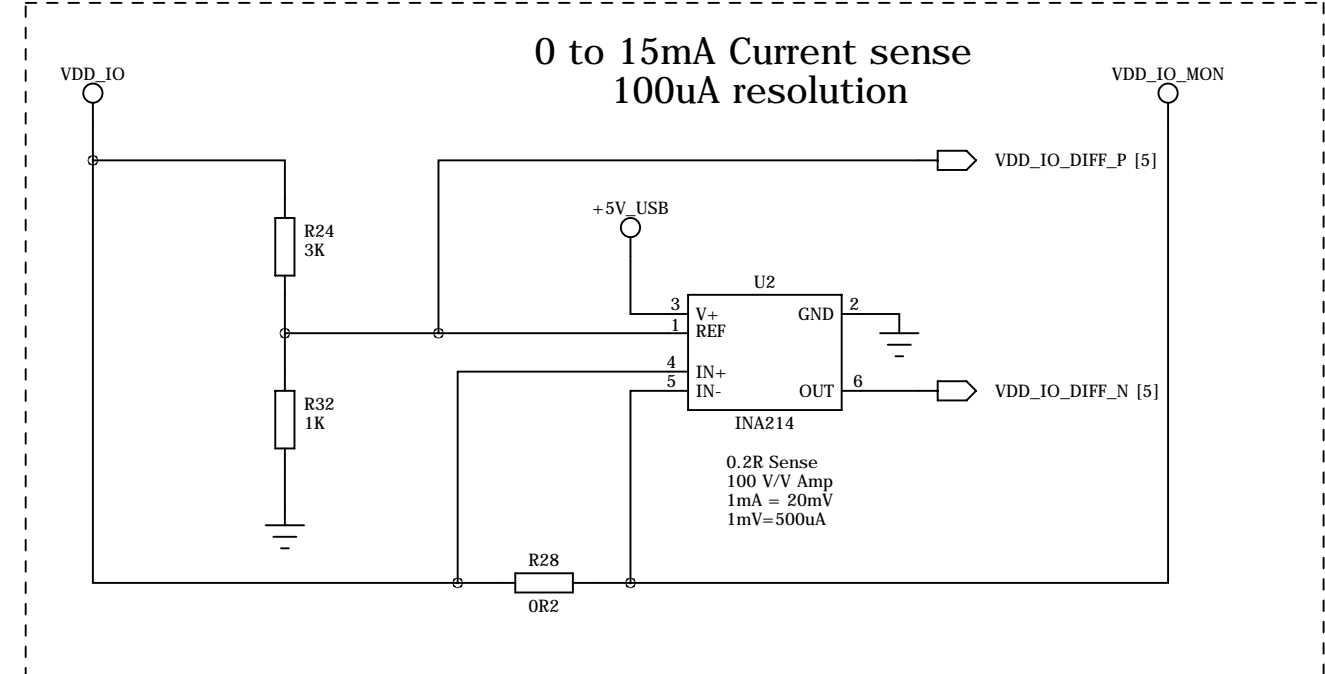
DAI Header GND Connections



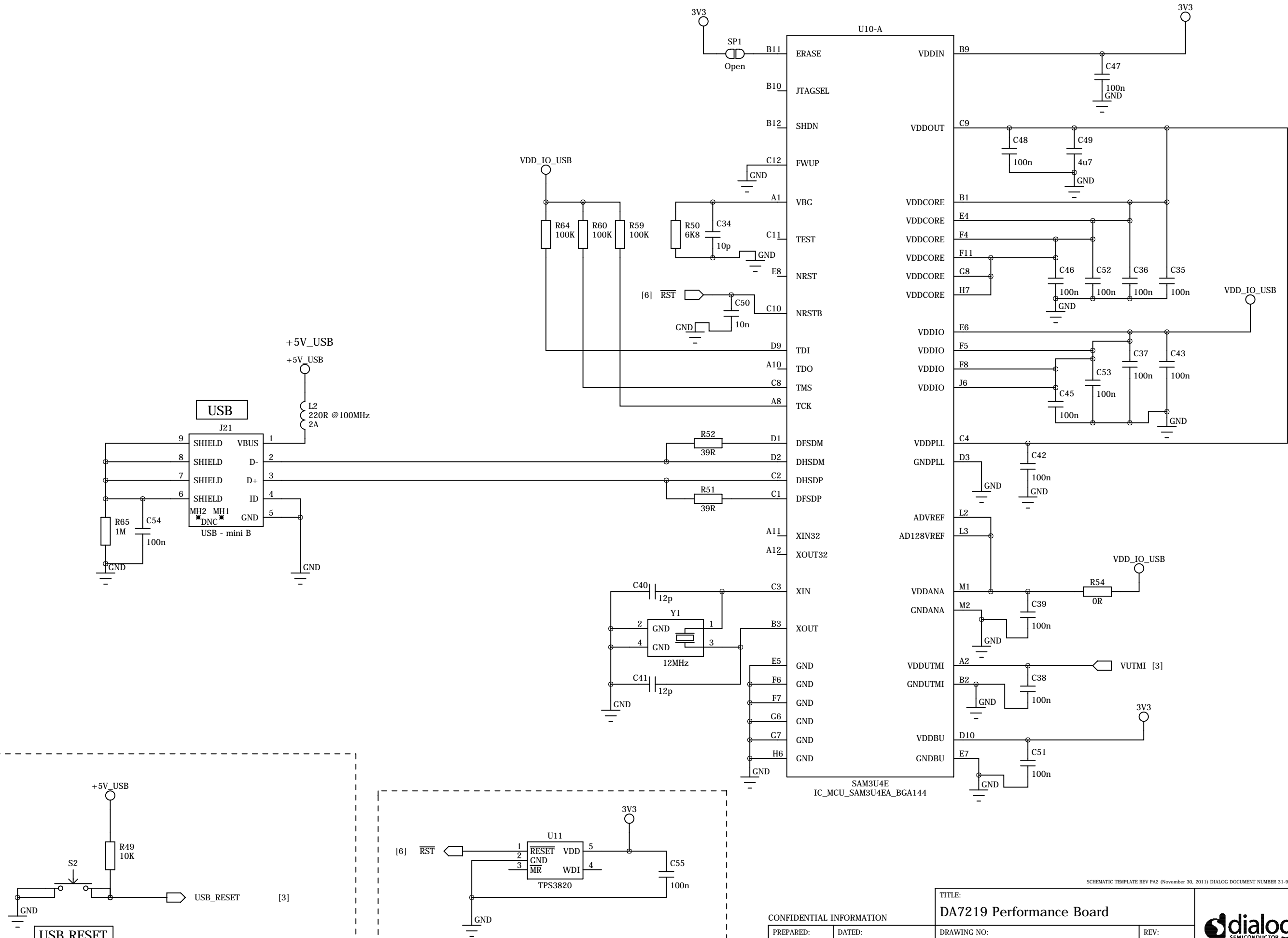
MCLK
SELECT



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PREPARED: CM	DATED: 26/10/2015	DRAWING NO: 284-02-C		REV: C	
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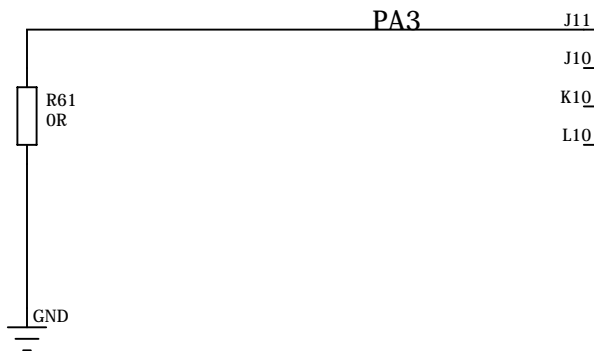
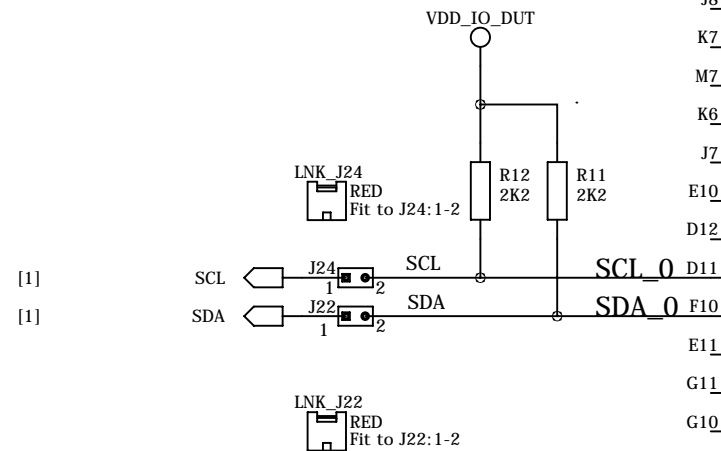
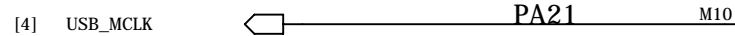
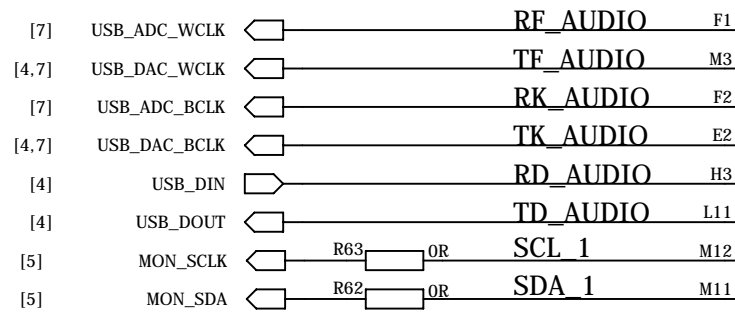
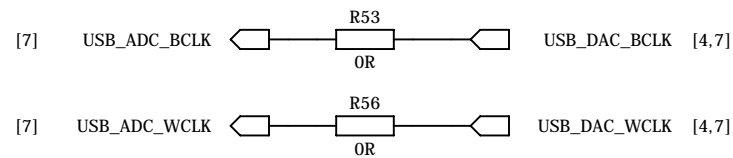
SCHEMATIC TEMPLATE REV PA2 (November 30, 2011) DIALOG DOCUMENT NUMBER 31-902-03

CONFIDENTIAL INFORMATION


PREPARED:	DATED:
CM	26/10/2015
RESPONSIBLE:	DATED:
CM	26/10/2015

TITLE:	
DA7219 Performance Board	
DRAWING NO:	REV:
284-02-C	C
SHEET NAME:	
USB	

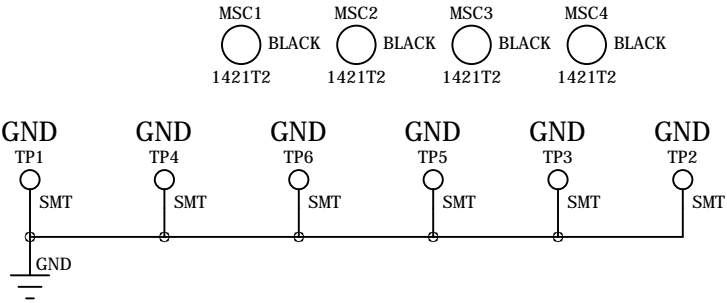
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SCHEMATIC TEMPLATE REV PA2 (November 30, 2011) DIALOG DOCUMENT NUMBER 31-902-03

CONFIDENTIAL INFORMATION		TITLE: DA7219 Performance Board		
PREPARED: CM	DATED: 26/10/2015	DRAWING NO: 284-02-C	REV: C	
RESPONSIBLE: CM	DATED: 26/10/2015	SHEET NAME: USB-IO		PAGE: 7 OF 8

REV	DATE	ENGINEER	NOTES
C	26/10/2015	CM	Approved Version



CONFIDENTIAL INFORMATION		TITLE: DA7219 Performance Board		
PREPARED: CM	DATED: 26/10/2015	DRAWING NO: 284-02-C	REV: C	
RESPONSIBLE: CM	DATED: 26/10/2015	SHEET NAME: REV_HISTORY/MECH		PAGE: 8 OF 8