
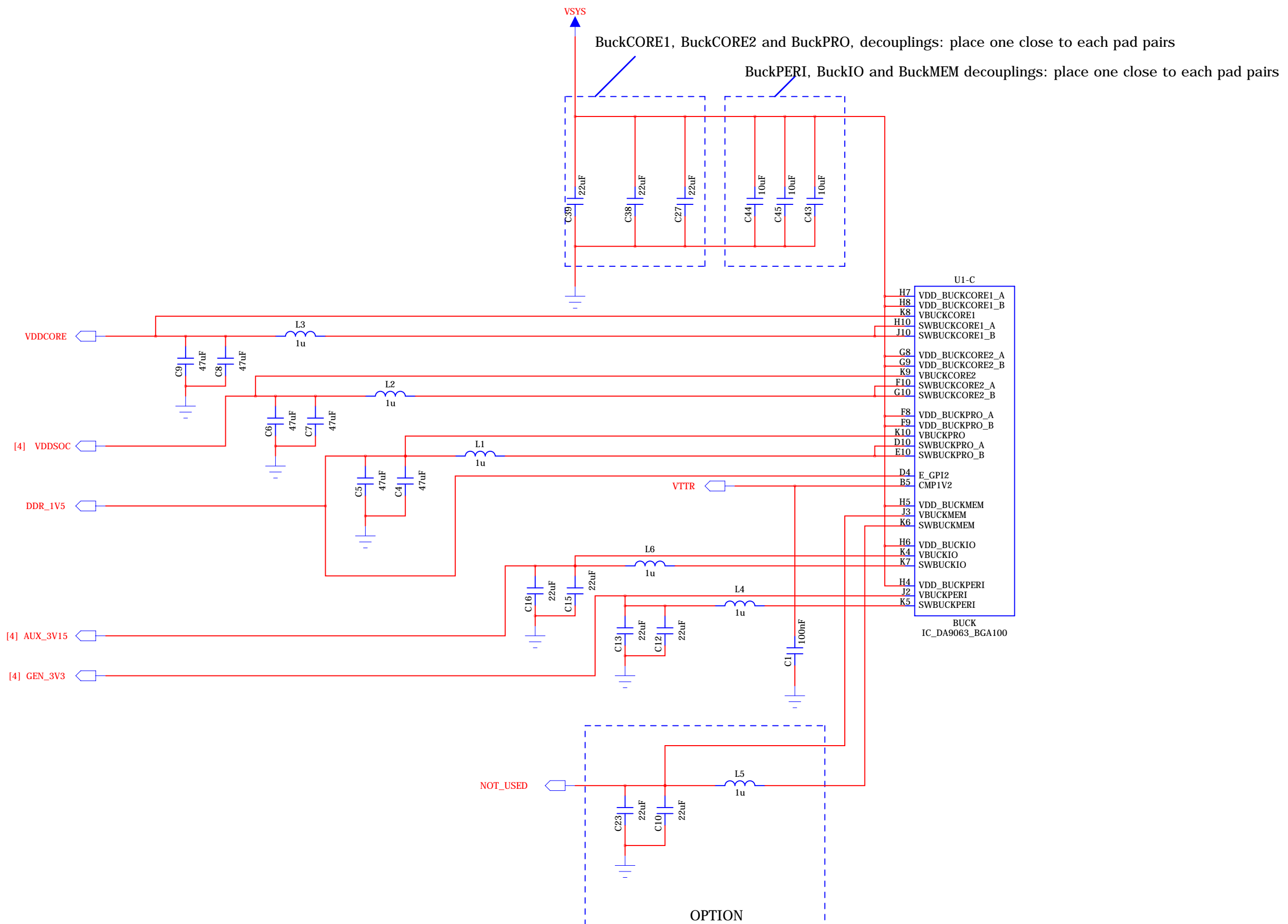


CONFIDENTIAL INFORMATION				TITLE: FREESCALE IMX6 TO DA9063 INTERCONN.	
PREPARED: FG	DATED: 11/06/2014	DRAWING NO: 44-179-176-08		REV: E	
RESPONSIBLE: FG	DATED: 11/07/2013	SHEET NAME: BLOCK_DIAGRAM		PAGE: 1 OF 5	



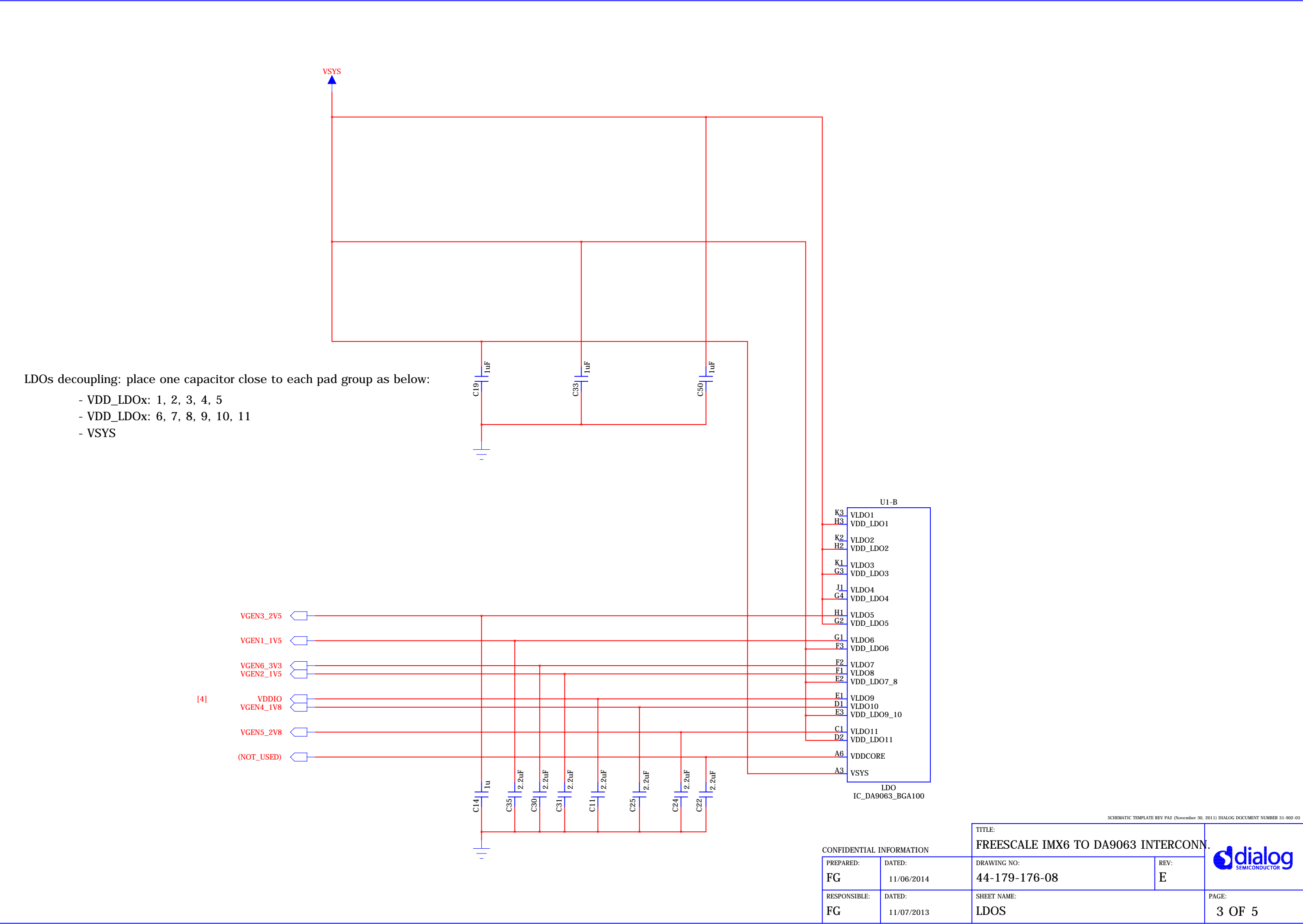
NOTE: the above configuration BuckPRO is used to generate VDDQ for the DDR supply. Therefore, no VTT terminations supply is available.

CONFIDENTIAL INFORMATION

PREPARED:	DATED:
FG	11/06/2014
RESPONSIBLE:	DATED:
FG	11/07/2013

TITLE:		FREESCALE IMX6 TO DA9063 INTERCONN.	
DRAWING NO:	44-179-176-08	REV:	E
SHEET NAME:	BUCKS	PAGE:	2 OF 5





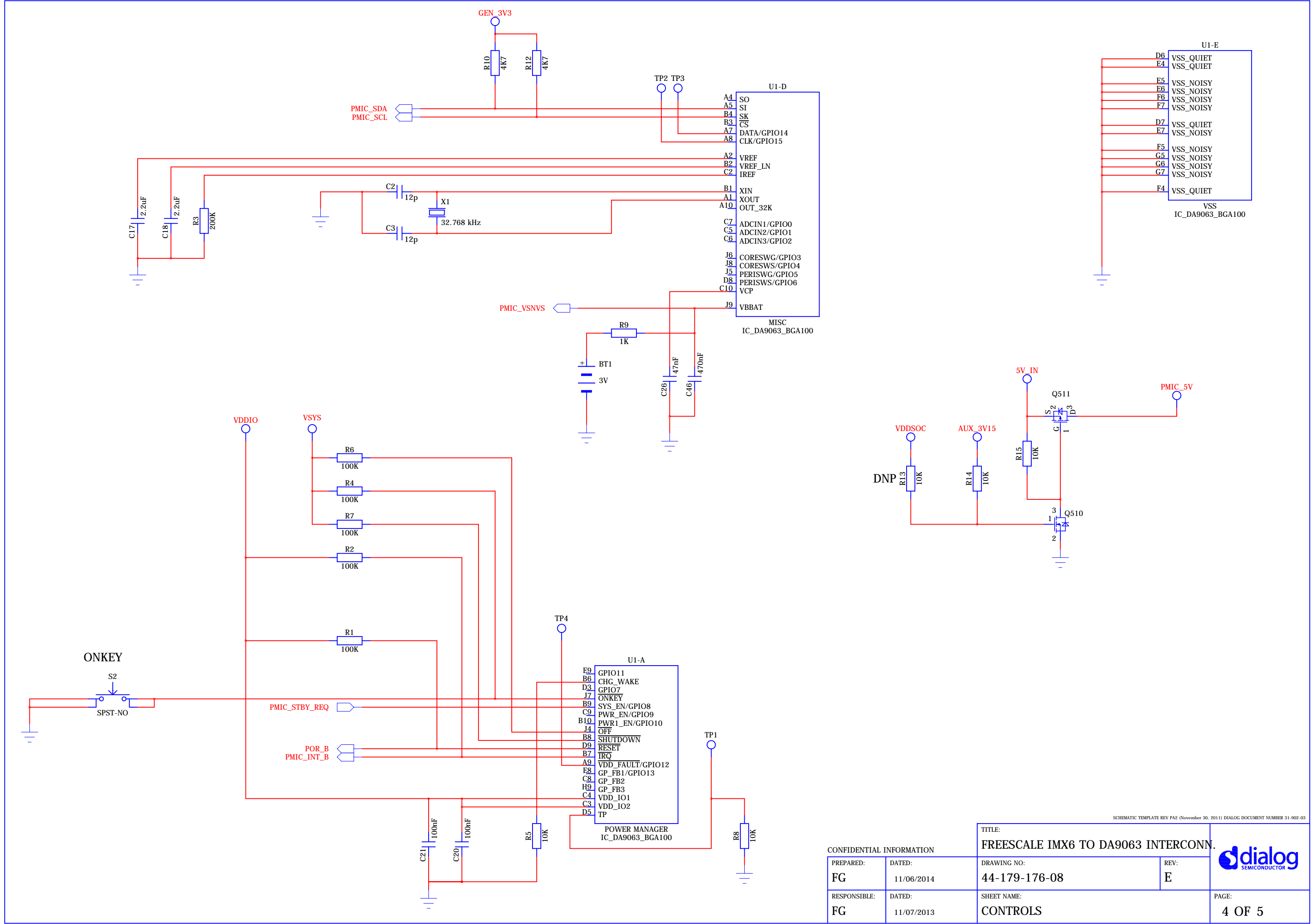
SCHEMATIC TEMPLATE REV PA2 (November 30, 2011) DIALOG DOCUMENT NUMBER 31-902-03

CONFIDENTIAL INFORMATION

PREPARED:	DATED:
FG	11/06/2014
RESPONSIBLE:	DATED:
FG	11/07/2013

TITLE:		FREESCALE IMX6 TO DA9063 INTERCONN.	
DRAWING NO:	44-179-176-08	REV:	E
SHEET NAME:	LDOS		
PAGE:	3 OF 5		





CONFIDENTIAL INFORMATION

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FG	11/06/2014
RESPONSIBLE:	DATED:
FG	11/07/2013

TITLE:		FREESCALE IMX6 TO DA9063 INTERCONN.	
DRAWING NO:	44-179-176-08	REV:	E
SHEET NAME:	CONTROLS		
PAGE:	4 OF 5		



REV	DATE	ENGINEER	NOTES
A	11 FEB 2013	F. GIULIANO	Initial version based on 44-179-176-07-A.
A	12 FEB 2013	F. GIULIANO	Added TP on GPIO14/15/12
A	15 FEB 2013	F. GIULIANO	Minor BOM changes
B	16 MAY 2013	F. GIULIANO	Updated DDR_VREF solution with divider on VDDREF
B	24 MAY 2013	F. GIULIANO	Included VLDO5 for VGEN3_2V5
C	30th JULY 2013	F. GIULIANO	Amended nSHUTDOWN and PMIC_ON REQ control lines. Pullups on PMIC_VSNVS rail now on VLDOCORE. Pull ups on I2C
D	13th August 2013	F. GIULIANO	Added 0R links on MX6_ONOFF line. Added nONKEY button.
E	10th June 2014	F. GIULIANO	Updated to reflect the PEBIX board and be compatible with 3E_F04F variant

NOTES:

- 1) this schematic must be used in conjunction with the Application Note AN-PM-027
- 2) The system architecture has been designed to work in conjunction with a DA9063 - 3E variant.

For details refer to the AN-PM-027. For use with other variants, please contact Dialog Semiconductor