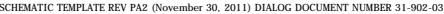


CONFIDENTIAL INFORMATION

PREPARED:	DATED:
BL	23/10/2012
RESPONSIBLE:	DATED:
MV	19/10/2012

TITLE:		Hippo Motherboard	
DRAWING NO:	44-179-176-05	REV:	B
SHEET NAME:		BOARD_IF_HEADERS	
PAGE:		1 OF 8	

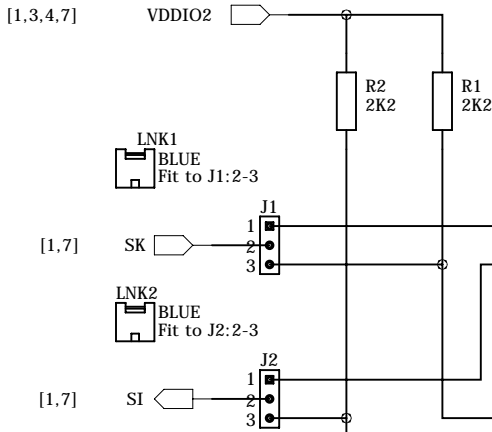
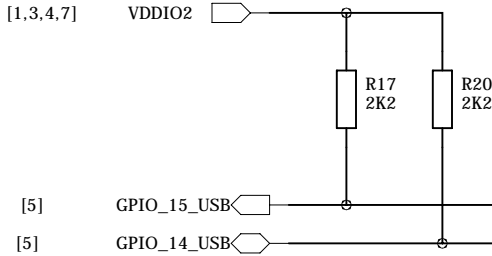




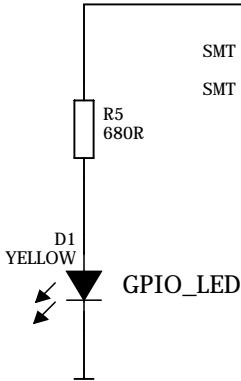
CONFIDENTIAL INFORMATION

TITLE:	
Hippo Motherboard	
DRAWING NO:	REV:
44-179-176-05	B
SHEET NAME:	
USB_IF	

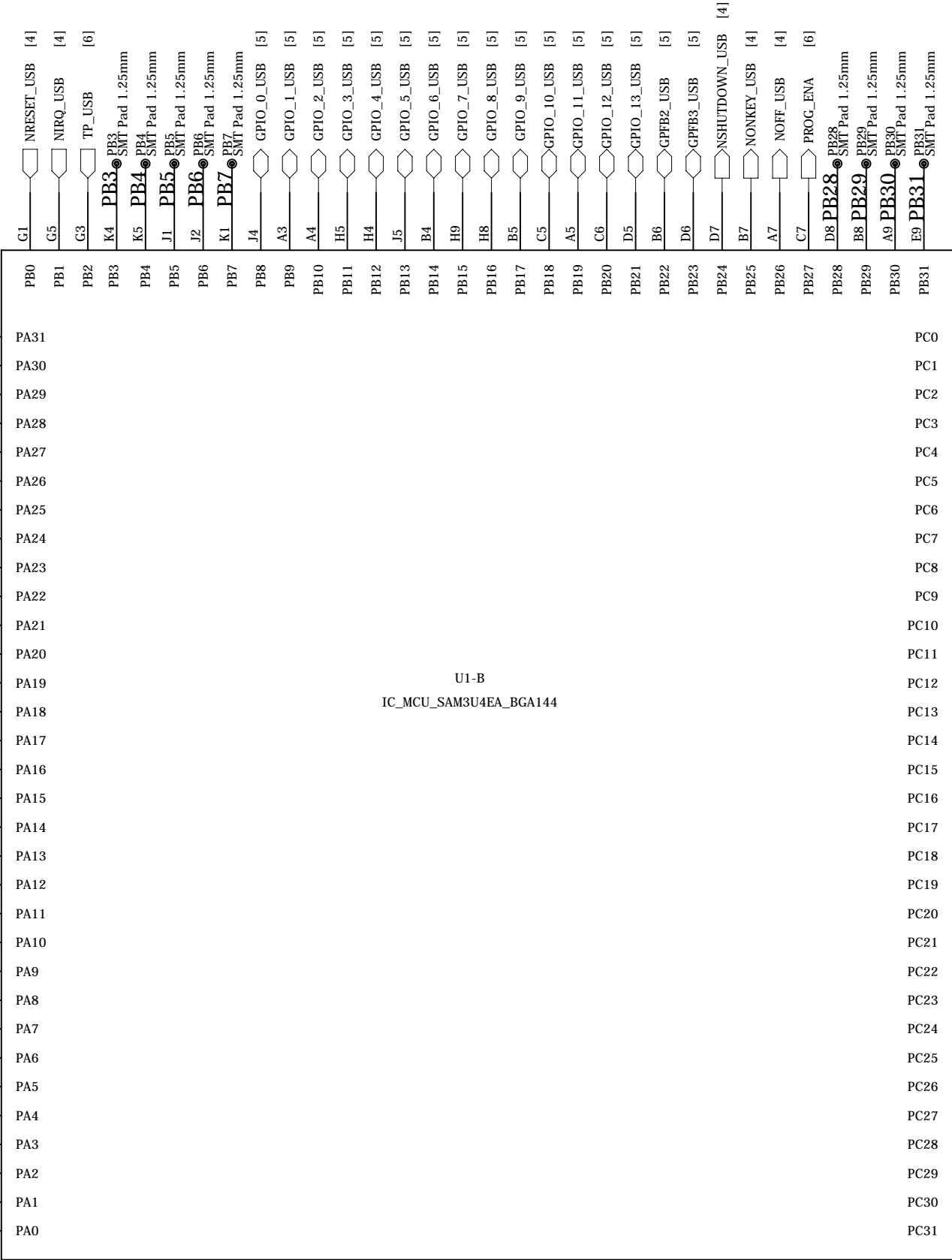




J1, J2	Control IF Select
1 - 2	4-wire (SPI)
2 - 3	2-wire (I2C)




PA31	RF_AUDIO	F1
PA30	TF_AUDIO	M3
PA29	RK_AUDIO	F2
PA28	TK_AUDIO	E2
PA27	RD_AUDIO	H3
PA26	TD_AUDIO	L11
PA25	SCL_1	M12
PA24	SDA_1	M11
PA23	PA23	L9
PA22	PA22	L4
PA21	PA21	M10
PA20	PA20	M9
PA19	SPI_NCS_3	L8
PA18	PA18	K8
PA17	PA17	J8
PA16	SPI_NCS_0	K7
PA15	SPI_SCLK	M7
PA14	SPI_MOSI	K6
PA13	SPI_MISO	J7
PA12	PA12	E10
PA11	PA11	D12
PA10	SCL_0	D11
PA9	SDA_0	F10
PA8	PA8	E11
PA7	M_TMS	G11
PA6	M_TCK	G10
PA5	M_TDO	H11
PA4	M_TDI	J12
PA3	PA3	J11
PA2	GP_LED(ALIVE)	J10
PA1	SPI_NCS_2	K10
PA0	SPI_NCS_1	L10

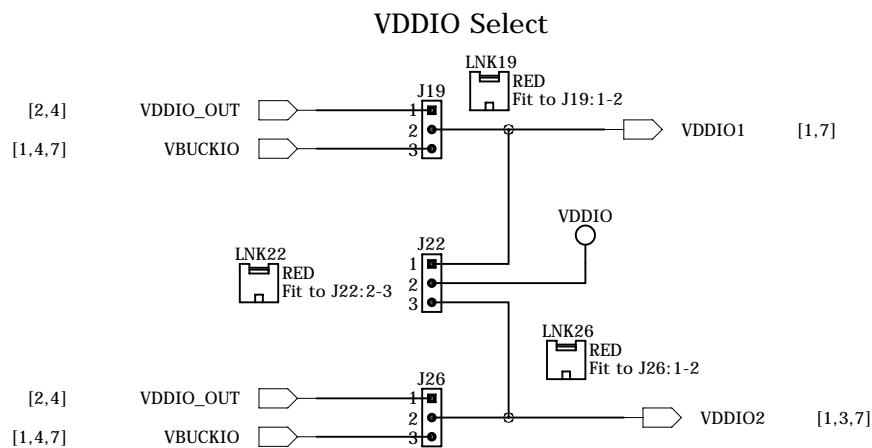
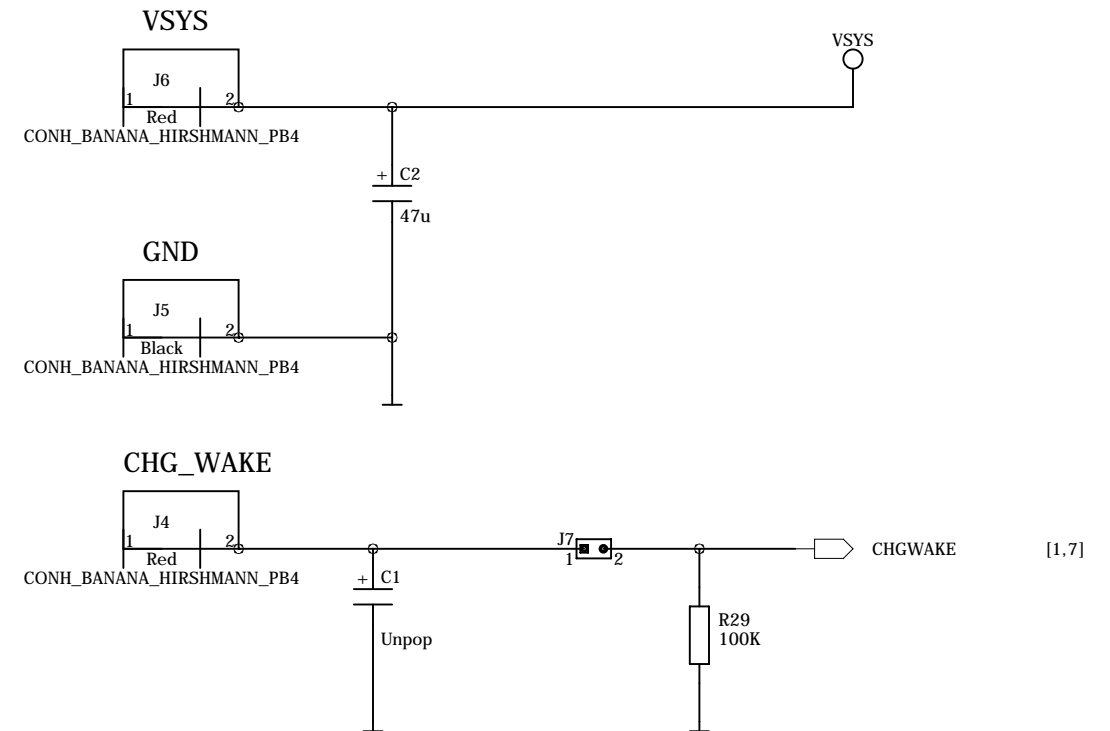
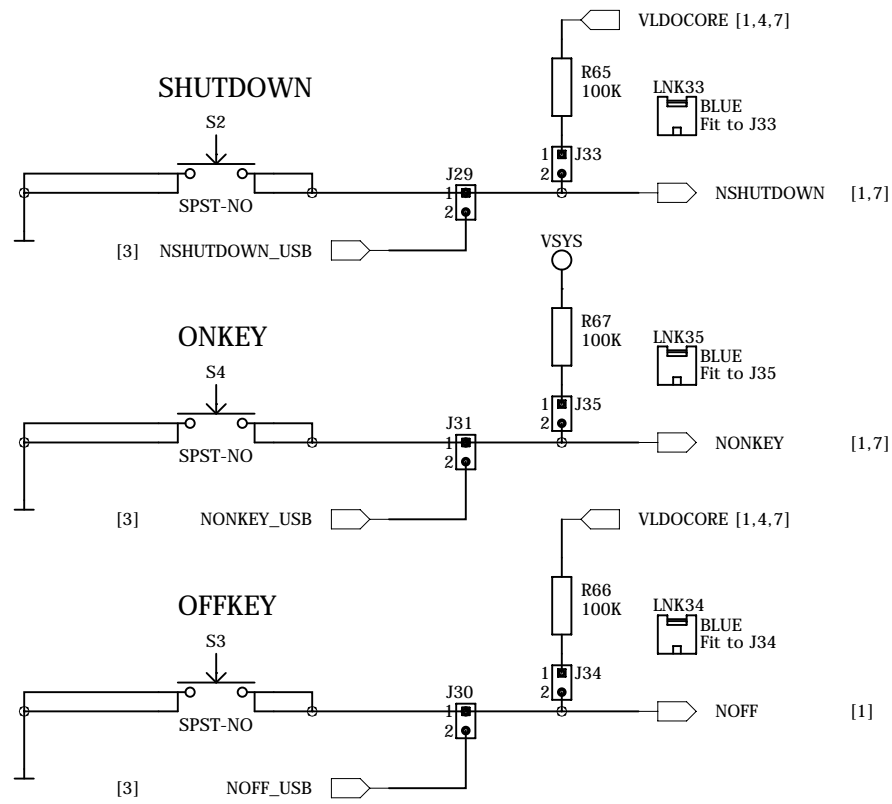


CONFIDENTIAL INFORMATION

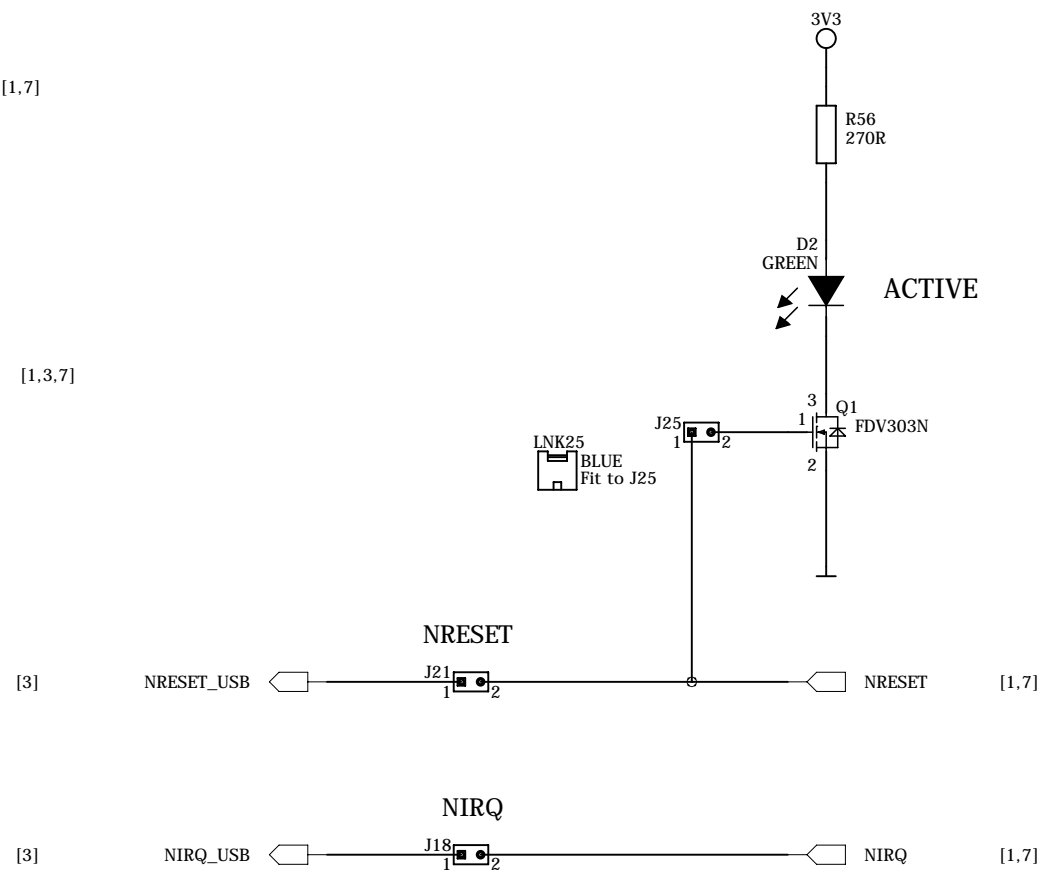
PREPARED: BL	DATED: 23/10/2012
RESPONSIBLE: MV	DATED: 19/10/2012

TITLE: Hippo Motherboard	
DRAWING NO: 44-179-176-05	REV: B
SHEET NAME: USB_LEVEL_SHIFTER	

	PAGE: 3 OF 8



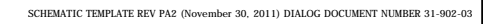
J19, J26	VDDIO Select
1 - 2	VDDIO_OUT (from USB)
2 - 3	VBUCKIO (from DUT)

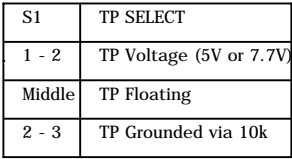


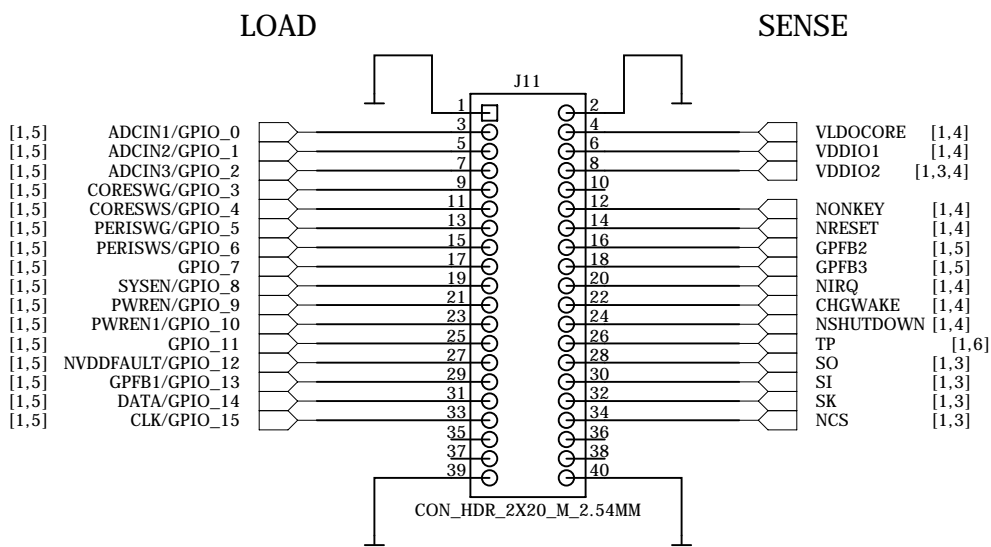
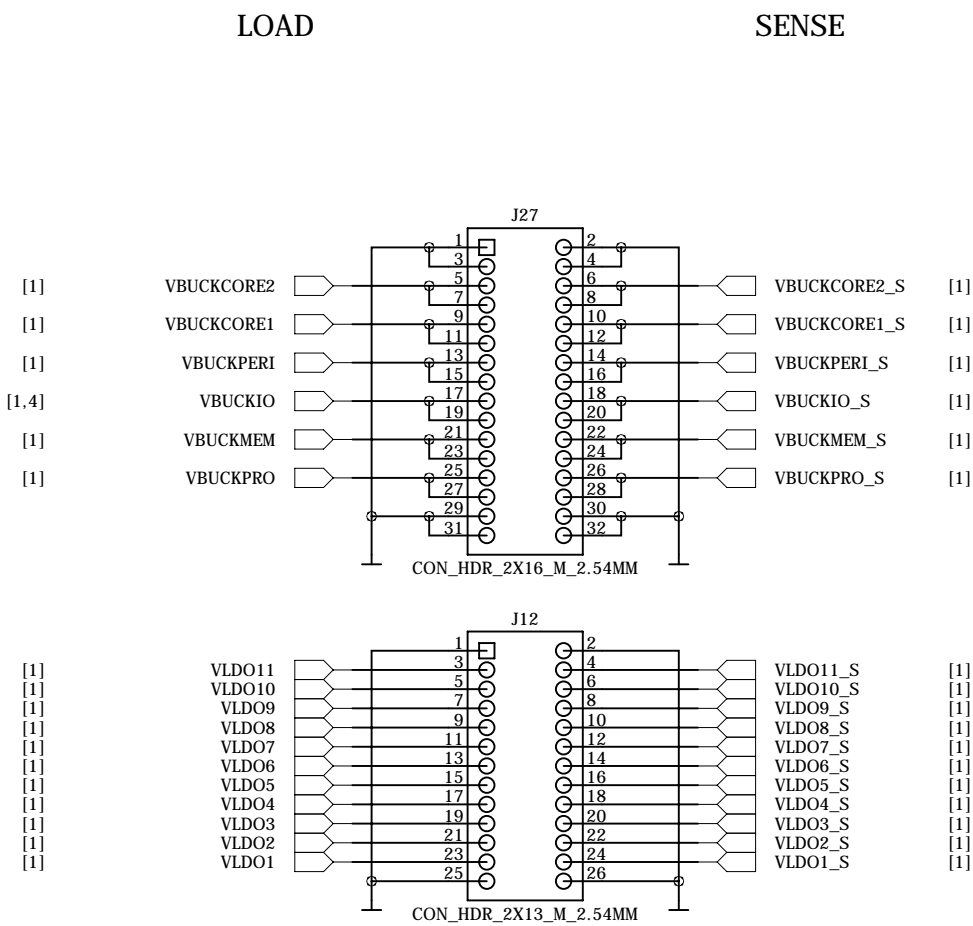
CONFIDENTIAL INFORMATION

PREPARED: BL	DATED: 23/10/2012
RESPONSIBLE: MV	DATED: 19/10/2012

TITLE: Hippo Motherboard		REV: B	
DRAWING NO: 44-179-176-05			
SHEET NAME: IO_POWER		PAGE: 4 OF 8	







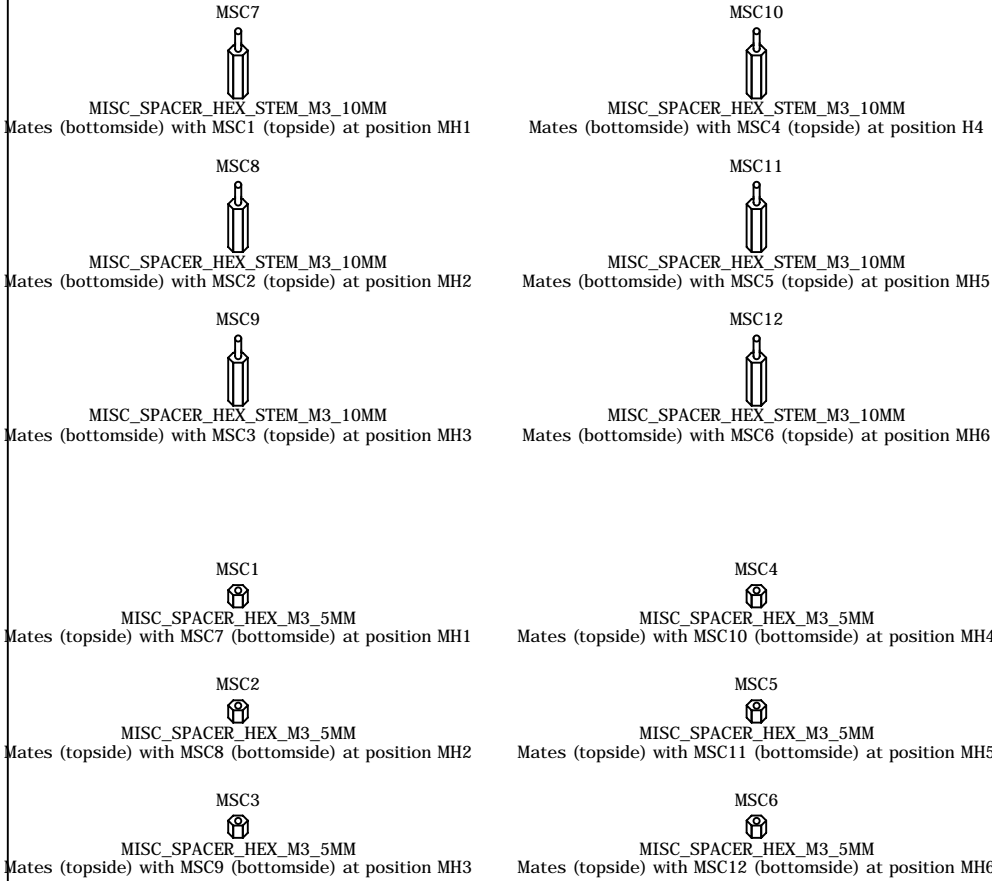
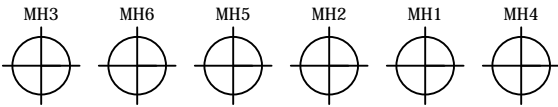
CONFIDENTIAL INFORMATION

PREPARED:	DATED:
BL	23/10/2012
RESPONSIBLE:	DATED:
MV	19/10/2012


TITLE:		Hippo Motherboard	
DRAWING NO:	44-179-176-05	REV:	B
SHEET NAME:	PMIC_HEADERS	PAGE:	7 OF 8



REV	DATE	ENGINEER	NOTES
0.1	19 April 2012	MV	Initial version based on 44-179-176-05-A: PROG ENA control changed, U1 VDDANA changed to VDDIO_USB and 100k pull-down added to CHGWAKE
B	23 October 2012	MV/BV/BL	Switches S2 & S3 - pulled up to VLDOCORE / Updated to match ECAD2PDM / Manufactured Version



SCHMATIC TEMPLATE REV PA2 (November 30, 2011) DIALOG DOCUMENT NUMBER 31-902-03

CONFIDENTIAL INFORMATION		TITLE:		
		Hippo Motherboard		
PREPARED:	DATED:	DRAWING NO:	REV:	
BL	23/10/2012	44-179-176-05	B	
RESPONSIBLE:	DATED:	SHEET NAME:		PAGE:
MV	19/10/2012	MECH_REVHIST		8 OF 8

SCHEMATIC TEMPLATE REV PA2 (November 30, 2011) DIALOG DOCUMENT NUMBER 31-902-03