

Description

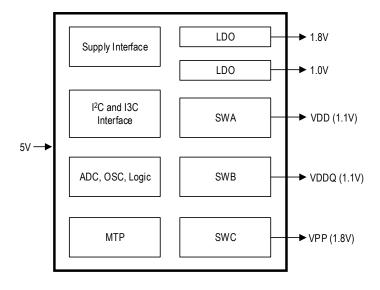
The P8911 is a power management IC (PMIC) designed for notebook, desktop, and embedded computing platforms. IDT's P8911 enables next-generation client platforms to take full advantage of DDR5 memory to realize greater performance, density, and reliability while reducing overall system power.

The P8911 has been optimized from the previously announced P8900 server PMIC to meet the specific needs of the client and embedded application spaces. With over 50% reduction in package footprint versus the P8900, the P8911 also features power delivery characteristics tailored toward smaller form factor memory modules that reside in systems that may operate on battery power for extended periods.

Typical Applications

Small outline and unbuffered DDR5-based dual in-line memory modules (DIMM)

Block Diagram



Features

- VIN_Bulk input supply range: 4.25V to 5.5V
- Three step-down switching regulators: SWA, SWB, SWC
- Programmable dual phase and single phase regulator for SWA and SWB
- Multi-Time Programmable Non-Volatile Memory
- Error injection capability
- Persistent error log registers
- Two LDO regulators: VOUT_1.8V, VOUT_1.0V
- Secure Mode and Programmable mode of operation
- Independently programmable output voltages, power-up, and power-down sequence for switch regulators
- Output power good status reporting mechanism
- VIN_Bulk input supply protection feature: Input over voltage
- Flexible Open Drain IO (I²C) and Push Pull (I3C Basic) IO Support
- Flexible mechanism to enable switch regulators (w/VR_EN pin or VR Enable command on I²C or I3C Basic interface)
- Idle Power State (P1 State)
- 28-QFN, 3 × 4 mm package

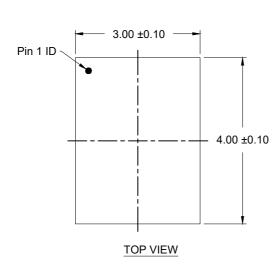
Package Outline Drawing

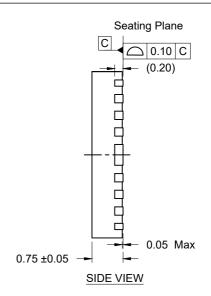


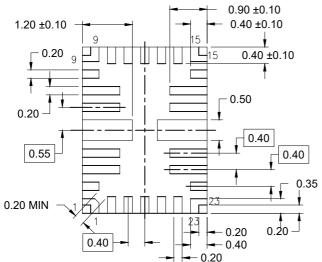
PSC-4818-01 FNG28D1

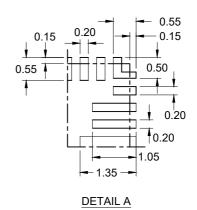
28-VFQFPN 3.0 x 4.0 x 0.75 mm Body, 0.4 mm Pitch (Min)

Rev.05, Jun 13, 2025

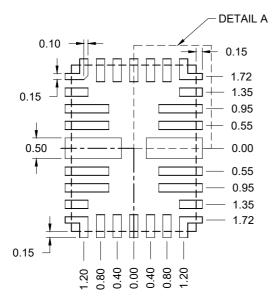








BOTTOM VIEW



NOTES:

- 1. JEDEC compatible.
- 2. All dimensions are in mm and angles are in degrees.
- 3. Use ± 0.05 mm for the non-toleranced dimensions.
- 4. Numbers in () are for references only.

RECOMMENDED LAND PATTERN (PCB Top View, NSMD Design)

© Renesas Electronics Corporation

IMPORTANT NOTICE AND DISCLAIMER

RENESAS ELECTRONICS CORPORATION AND ITS SUBSIDIARIES ("RENESAS") PROVIDES TECHNICAL SPECIFICATIONS AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT OF THIRD-PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for developers who are designing with Renesas products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. Renesas grants you permission to use these resources only to develop an application that uses Renesas products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Renesas intellectual property or to any third-party intellectual property. Renesas disclaims responsibility for, and you will fully indemnify Renesas and its representatives against, any claims, damages, costs, losses, or liabilities arising from your use of these resources. Renesas' products are provided only subject to Renesas' Terms and Conditions of Sale or other applicable terms agreed to in writing. No use of any Renesas resources expands or otherwise alters any applicable warranties or warranty disclaimers for these products.

(Disclaimer Rev.1.01)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan www.renesas.com

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit www.renesas.com/contact-us/.