

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RA*-A0145A/E	Rev.	1.00
Title	Correction of typo in IWDTRPSS bit in Independent Watchdog Timer(IWDT)		Information Category	Technical Notification		
Applicable Product	RA2L1 Group RA2E1 Group RA2E2 Group RA2A1 Group	Lot No.	Reference Document	-RA2L1 Group User's Manual: Hardware R01UH0853EJ0150 Rev.1.50 Aug.2024 -RA2E1 Group User's Manual: Hardware R01UH0852EJ0150 Rev.1.50 Jun.2024 -RA2E2 Group User's Manual: Hardware R01UH0919EJ0140 Rev.1.40 Sep.2024 -RA2A1 Group User's Manual: Hardware R01UH0888EJ0110 Rev.1.10 Jul.2023		
		All lots				

The following corrections have been made to the RA2L1, RA2E1, RA2E2, and RA2A1 User's manuals:hardware.

Correction details

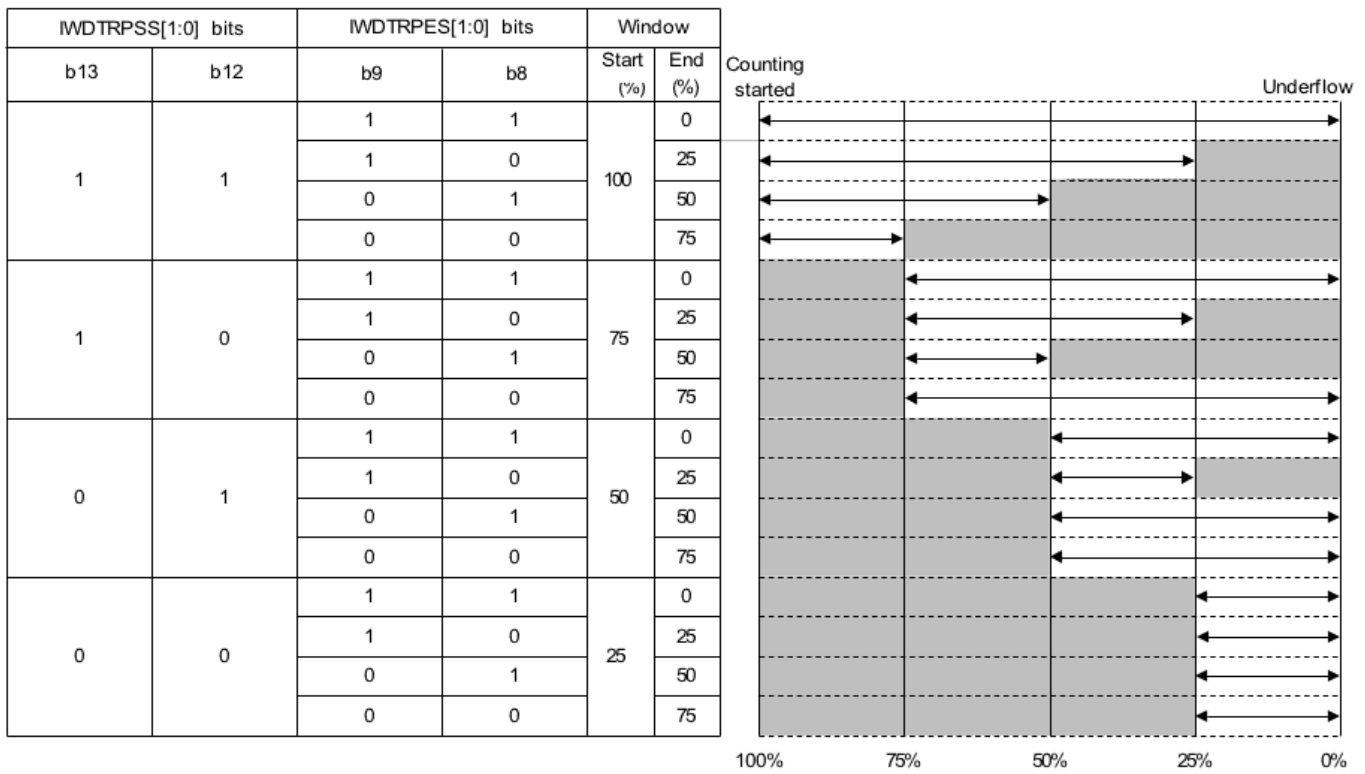
Document	Correction page and section	Contents
RA2L1 Group	P.507 Figure 24.2 IWDTRPSS[1:0] and IWDTRPES[1:0] bit settings and refresh-permitted period	Correction
RA2E1 Group	P.498 Figure 24.2 IWDTRPSS[1:0] and IWDTRPES[1:0] bit settings and refresh-permitted period	Correction
RA2E2 Group	P.423 Figure 23.2 IWDTRPSS[1:0] and IWDTRPES[1:0] bit settings and refresh-permitted period	Correction
RA2A1 Group	P.514 Figure 25.2 IWDTRPSS[1:0] and IWDTRPES[1:0] bit settings and refresh-permitted period	Correction

Document Improvement Plan

These corrections will be made in the next revision of the user's manuals.

Figure IWDTRPSS [1:0] and IWDTRPES [1:0] bits settings and refresh-permitted period will be updated as follows.

Before



After

