

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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38K2 Group Usage Notes Concerning Port P2

Classification

Corrections and supplementary explanation of document

- ✓ Notes
- Knowhow
- Others

Concerned Products

M38K29RFS, M38K29F8FP, M38K29F8LFP, M38K29F8HP, M38K29F8LHP, M38K27M4-XXXFP, M38K27M4L-XXXFP, M38K27M4-XXXHP, M38K27M4L-XXXHP

1. Notes

In the 38K2 Group, do not set bits of the direction register corresponding to ports P2₀–P2₃ (bits 0–3 of port P2 direction register (address 05₁₆)) to output mode (“1”). If writing to these bits, write “0”.

2. Concerned User’s Manual

English (PDF Rev.1.0): Page 2-3 Fig.2.1.3, Page 3-51 Fig.3.5.2

Japanese (PDF Rev.1.0): Page 2-3 Fig.2.1.3, Page 3-48 Fig.3.5.2

Port Pi direction register

b7 b6 b5 b4 b3 b2 b1 b0



Port Pi direction register (PiD) (i = 0, 1, 2, 3, 4, 5, 6) (Note)
[Address : 01₁₆, 03₁₆, 05₁₆, 07₁₆, 09₁₆, 0B₁₆, 0D₁₆]

B	Name	Function	At reset	R	W
0	Port Pi direction register	0 : Port Pi ₀ input mode 1 : Port Pi ₀ output mode	0	×	○
1		0 : Port Pi ₁ input mode 1 : Port Pi ₁ output mode	0	×	○
2		0 : Port Pi ₂ input mode 1 : Port Pi ₂ output mode	0	×	○
3		0 : Port Pi ₃ input mode 1 : Port Pi ₃ output mode	0	×	○
4		0 : Port Pi ₄ input mode 1 : Port Pi ₄ output mode	0	×	○
5		0 : Port Pi ₅ input mode 1 : Port Pi ₅ output mode	0	×	○
6		0 : Port Pi ₆ input mode 1 : Port Pi ₆ output mode	0	×	○
7		0 : Port Pi ₇ input mode 1 : Port Pi ₇ output mode	0	×	○

Note: Since the following ports are not allocated, the corresponding bits can not be used.

- P2₀ to P2₃
- P4₄ to P4₇
- P6₄ to P6₇

Do not set bits of the direction register corresponding to ports P2₀–P2₃ (bits 0–3 of port P2 direction register (address 05₁₆)) to output mode (“1”). If writing to these bits, write “0”.

Structure of Port Pi Direction Register (i = 0 to 6)