

[Upgrade to Revision]
 Programming GUI for PG-FP6 Flash Memory Programmer
 FP6 Terminal V1.17.00

R20TS1148EJ0100
 Rev.1.00
 Jul. 20, 2025

Outline

FP6 Terminal, a programming GUI for PG-FP6 Flash Memory Programmer, has been updated from V1.16.00 to V1.17.00. For an overview of PG-FP6, refer to the link below.

<https://www.renesas.com/pg-fp6>

Connect the updated FP6 Terminal to PG-FP6 to update the firmware of PG-FP6.

1. Description

The main changes are as follows. Refer to the release note for details.

(The note is applicable as of July 22.)

<https://www.renesas.com/software-tool/pg-fp6#document>

1.1 Addition of Supported Devices

- RA Family

Group	Part number
RA8P1	R7KA8P1AD, R7KA8P1AF, R7KA8P1BD, R7KA8P1BF, R7KA8P1JF, R7KA8P1KF

- RH850 Family

Group	Part number
RH850/F1KM	R7F701A61, R7F701A62, R7F701A63

- Renesas USB Power Delivery Family

Group	Part number
C30	R9A02G0150

1.2 New feature

- Technical support

A link to the [Technical Support] page on the Renesas Web site was added to the [Help] menu on the menu bar. If you need a reply from us, you can send your questions from there.

1.3 Improved Functionality and Change

- Change to the initial value of the offset when a binary file is specified

Previously, when a binary file was specified, the initial value of the offset was "00000000". The specification has now been changed so that the start address of memory in the target device is displayed.

2. Updating the Product

FP6 Terminal can be updated free of charge. Download the installer from the link below.

(The installer is available as of July 22.)

<https://www.renesas.com/software-tool/pg-fp6#download>

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Jul.20.25	-	First edition issued

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